

Decision Diagram Techniques for Micro- and Nanoelectronic Design

H A N D B O O K

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Decision Diagram Techniques For Micro And Nanoelectronic Design Handbook

**Anatoly Lisnianski, Ilia Frenkel, Alex
Karagrigoriou**



Decision Diagram Techniques For Micro And Nanoelectronic Design Handbook:

Decision Diagram Techniques for Micro- and Nanoelectronic Design Handbook, 2006 **Decision Diagram Techniques for Micro- and Nanoelectronic Design Handbook** Svetlana N. Yanushkevich, D. Michael Miller, Vlad P. Shmerko, Radomir S. Stankovic, 2005-12-22 Decision diagram DD techniques are very popular in the electronic design automation EDA of integrated circuits and for good reason They can accurately simulate logic design can show where to make reductions in complexity and can be easily modified to model different scenarios Presenting DD techniques from an applied perspective Decision Diagram Techniques for Micro and Nanoelectronic Design Handbook provides a comprehensive up to date collection of DD techniques Experts with more than forty years of combined experience in both industrial and academic settings demonstrate how to apply the techniques to full advantage with more than 400 examples and illustrations Beginning with the fundamental theory data structures and logic underlying DD techniques they explore a breadth of topics from arithmetic and word level representations to spectral techniques and event driven analysis The book also includes abundant references to more detailed information and additional applications Decision Diagram Techniques for Micro and Nanoelectronic Design Handbook collects the theory methods and practical knowledge necessary to design more advanced circuits and places it at your fingertips in a single concise reference *Nanophotonic Information Physics* Makoto Naruse, 2013-12-12 This book provides a new direction in the field of nano optics and nanophotonics from information and computing related sciences and technology Entitled by Information Physics and Computing in Nanoscale Photonics and Materials IPCN in short the book aims to bring together recent progresses in the intersection of nano scale photonics information and enabling technologies The topic will include 1 an overview of information physics in nanophotonics 2 DNA self assembled nanophotonic systems 3 Functional molecular sensing 4 Smart fold computing an architecture for nanophotonics 5 semiconductor nanowire and its photonic applications 6 single photoelectron manipulation in imaging sensors 6 hierarchical nanophotonic systems 8 photonic neuromorphic computing and 9 SAT solver and decision making based on nanophotonics New Advances in Dependability of Networks and Systems Wojciech Zamojski, Jacek Mazurkiewicz, Jarosław Sugier, Tomasz Walkowiak, Janusz Kacprzyk, 2022-05-26 The book consists of papers on selected topics of dependability analysis in computer systems and networks which were discussed during the 17th DepCoS RELCOMEX conference held in Wrocław Poland from June 27th to July 1st 2022 Their collection will be an interesting source material for scientists researchers practitioners and students who are dealing with design analysis and engineering of computer systems and networks and must ensure their dependable operation Being probably the most complex technical systems ever engineered by man and also the most dynamically evolving ones organization of contemporary computer systems and networks cannot be interpreted only as a structure built on the base of unreliable technical resources Their evaluation must take into account a unique blend of interacting people networks together with mobile properties cloud organization Internet

of Everything etc and a large number of users dispersed geographically and constantly producing an unconceivable number of applications Research methods being continuously developed for dependability analyses apply newest results of artificial and computational intelligence Selection of papers in this book illustrates broad range of topics often multi disciplinary which is considered in present day dependability explorations it also reveals an increasing role of the latest methods based on machine deep learning and neural networks in these studies

Introduction to Noise-Resilient Computing Svetlana N. Yanushkevich,Seiya Kasai,Golam Tangim,A.H. Tran,2022-06-01 Noise abatement is the key problem of small scaled circuit design New computational paradigms are needed as these circuits shrink they become very vulnerable to noise and soft errors In this lecture we present a probabilistic computation framework for improving the resiliency of logic gates and circuits under random conditions induced by voltage or current fluctuation Among many probabilistic techniques for modeling such devices only a few models satisfy the requirements of efficient hardware implementation specifically Boltzman machines and Markov Random Field MRF models These models have similar built in noise immunity characteristics based on feedback mechanisms In probabilistic models the values 0 and 1 of logic functions are replaced by degrees of beliefs that these values occur An appropriate metric for degree of belief is probability We discuss various approaches for noise resilient logic gate design and propose a novel design taxonomy based on implementation of the MRF model by a new type of binary decision diagram BDD called a cyclic BDD In this approach logic gates and circuits are designed using 2 to 1 bi directional switches Such circuits are often modeled using Shannon expansions with the corresponding graph based implementation BDDs Simulation experiments are reported to show the noise immunity of the proposed structures Audiences who may benefit from this lecture include graduate students taking classes on advanced computing device design and academic and industrial researchers

Table of Contents Introduction to probabilistic computation models Nanoscale circuits and fluctuation problems Estimators and Metrics MRF Models of Logic Gates Neuromorphic models Noise tolerance via error correcting Conclusion and future work

Logic Functions and Equations Christian Posthoff,Bernd Steinbach,2018-12-31 The expanded and updated 2nd edition of this classic text offers the reader a comprehensive introduction to the concepts of logic functions and equations and their applications across computer science The approach emphasizes a thorough understanding of the fundamental principles as well as numerical and computer based solution methods Updated throughout some major additions for the 2nd edition include an expanded introductory section on logic equations a new chapter on sets lattices and classes of logic functions a new chapter about SAT problems a new chapter about methods to solve extremely complex problems and an expanded section with new decomposition methods utilizing the Boolean Differential Calculus extended to lattices of logic functions The book provides insight into applications across binary arithmetic coding complexity logic design programming computer architecture and artificial intelligence Based on the extensive teaching experience of the authors *Logic Functions and Equations* is highly recommended for a one or two semester course in computer science and related programs It

provides straightforward high level access to these methods and enables sophisticated applications elegantly bridging the gap between mathematics and the theoretical foundations of computer science

Spectral Logic and Its Applications for the Design of Digital Devices Mark G. Karpovsky, Radomir S. Stankovic, Jaakko T. Astola, 2008-05-27 Spectral techniques facilitate the design and testing of today's increasingly complex digital devices There is heightened interest in spectral techniques for the design of digital devices dictated by ever increasing demands on technology that often cannot be met by classical approaches Spectral methods provide a uniform and consistent theoretic environment for recent achievements in this area which appear divergent in many other approaches Spectral Logic and Its Applications for the Design of Digital Devices gives readers a foundation for further exploration of abstract harmonic analysis over finite groups in the analysis design and testing of digital devices After an introduction this book provides the essential mathematical background for discussing spectral methods It then delves into spectral logic and its applications covering Walsh Haar arithmetic transform Reed Muller transform for binary valued functions and Vilenkin Chrestenson transform generalized Haar and other related transforms for multiple valued functions Polynomial expressions and decision diagram representations for switching and multiple value functions Spectral analysis of Boolean functions Spectral synthesis and optimization of combinational and sequential devices Spectral methods in analysis and synthesis of reliable devices Spectral techniques for testing computer hardware This is the authoritative reference for computer science and engineering professionals and researchers with an interest in spectral methods of representing discrete functions and related applications in the design and testing of digital devices It is also an excellent text for graduate students in courses covering spectral logic and its applications

Recent Advances in Multi-state Systems Reliability Anatoly Lisnianski, Ilia Frenkel, Alex Karagrigoriou, 2017-08-12 This book addresses a modern topic in reliability multi state and continuous state system reliability which has been intensively developed in recent years It offers an up to date overview of the latest developments in reliability theory for multi state systems engineering applications to a variety of technical problems and case studies that will be of interest to reliability engineers and industrial managers It also covers corresponding theoretical issues as well as case studies illustrating the applications of the corresponding theoretical advances The book is divided into two parts Modern Mathematical Methods for Multi state System Reliability Analysis Part 1 and Applications and Case Studies Part 2 which examines real world multi state systems It will greatly benefit scientists and researchers working in reliability as well as practitioners and managers with an interest in reliability and performability analysis It can also be used as a textbook or as a supporting text for postgraduate courses in Industrial Engineering Electrical Engineering Mechanical Engineering Applied Mathematics and Operations Research

Introduction to Logic Design Svetlana N. Yanushkevich, Vlad P. Shmerko, 2008-01-25 With an abundance of insightful examples problems and computer experiments Introduction to Logic Design provides a balanced easy to read treatment of the fundamental theory of logic functions and applications to the design

of digital devices and systems Requiring no prior knowledge of electrical circuits or electronics it supplies the essential material to understand the basic operation and design of digital systems Satisfying the Requirements of an Introductory Course The text presents a clear picture of basic concepts effective problem solving techniques modern technologies and applications It focuses on the relationships between and the manipulation of various data structures By examining the similarities and differences between assorted theoretical approaches for the representation manipulation and optimization of Boolean functions the authors provide a unified overview of the relationships among digital system design computer organization micro and nanoelectronics and numerical methods The book also describes classical minimization techniques along with advanced and emerging problems of logic design Meeting the Needs of Developing Technology Reflecting the integrated nature of modern engineering this text shows how theoretical ideas physical devices and design methodologies come together to form a successful design approach It provides the basis to explore even more sophisticated aspects in the field **IEICE Transactions on Electronics** ,2008 *The British National Bibliography* Arthur James Wells,2006

Logic Design of NanoICS Svetlana N. Yanushkevich,Vlad P. Shmerko,Sergey Edward Lyshevski,2017-12-19 Today s engineers will confront the challenge of a new computing paradigm relying on micro and nanoscale devices Logic Design of NanoICs builds a foundation for logic in nanodimensions and guides you in the design and analysis of nanoICs using CAD The authors present data structures developed toward applications rather than a purely theoretical treatment Requiring only basic logic and circuits background Logic Design of NanoICs draws connections between traditional approaches to design and modern design in nanodimensions The book begins with an introduction to the directions and basic methodology of logic design at the nanoscale then proceeds to nanotechnologies and CAD graphical representation of switching functions and networks word level and linear word level data structures 3 D topologies based on hypercubes multilevel circuit design and fault tolerant computation in hypercube like structures The authors propose design solutions and techniques going beyond the underlying technology to provide more applied knowledge This design oriented reference is written for engineers interested in developing the next generation of integrated circuitry illustrating the discussion with approximately 250 figures and tables 100 equations 250 practical examples and 100 problems Each chapter concludes with a summary references and a suggested reading section **Structural Decision Diagrams in Digital Test** Raimund Ubar,Jaan Raik,Maksim

Jenihhin,Artur Jutman,2024-01-20 This is the first book that sums up test related modeling of digital circuits and systems by a new structural decision diagrams model The model represents structural and functional information jointly and opens a new area of research The book introduces and discusses applications of two types of structural decision diagrams DDs low level structurally synthesized binary DDs SSBDDs and high level DDs HLDDs that enable diagnostic modeling of complex digital circuits and systems Topics and features Provides the definition properties and techniques for synthesis compression and optimization of SSBDDs and HLDDs Provides numerous working examples that illustrate the key points of the text Describes

applications of SSBDs and HLDDs for various electronic design automation EDA tasks such as logic level fault modeling and simulation multi valued simulation timing critical path identification and test generation Discusses the advantages of the proposed model to traditional binary decision diagrams and other traditional design representations Combines SSBDs with HLDDs for multi level representation of digital systems for enabling hierarchical and cross level solving of complex test related tasks This unique book is aimed at researchers working in the fields of computer science and computer engineering focusing on test diagnosis and dependability of digital systems It can also serve as a reference for graduate and advanced undergraduate level computer engineering and electronics courses Three authors are affiliated with the Dept of Computer Systems at the Tallinn University of Technology Estonia Raimund Ubar is a retired Professor Jaan Raik and Maksim Jenihhin are tenured Professors Artur Jutman PhD is a researcher at the same university and the CEO of Testonica Lab Ltd Estonia

Exact Minimization of Binary Decision Diagrams Using Implicit Techniques Arlindo Manuel Limede de Oliveira,1996

Design and Implementation of Multiple-valued Decision Diagrams Based on a Binary Decision Diagram Package Kanish Viswanathan,2005 **Transformation Techniques for Decision Diagrams in Computer Aided Design** Thorsten

Theobald,1998 **Computer Aided Design of Micro- and Nanoelectronic Devices** Chinmay Kumar Maiti,2017 Micro and nanoelectronic devices are the prime movers for electronics which is essential for the current information age This unique monograph identifies the key stages of advanced device design and integration in semiconductor manufacturing It brings into one resource a comprehensive device design using simulation The book presents state of the art semiconductor device design using the latest TCAD tools Professionals researchers academics and graduate students in electrical electronic engineering and microelectronics will benefit from this reference text The Binary Decision Diagram José Alfredo Ribeiro da Silva Matos,1983 Introduction to Microelectronics to Nanoelectronics Manoj Kumar Majumder,Vijay Rao

Kumbhare,Aditya Japa,Brajesh Kumar Kaushik,2020-11-24 Focussing on micro and nanoelectronics design and technology this book provides thorough analysis and demonstration starting from semiconductor devices to VLSI fabrication designing analog and digital on chip interconnect modeling culminating with emerging non silicon nano devices It gives detailed description of both theoretical as well as industry standard HSPICE Verilog Cadence simulation based real time modeling approach with focus on fabrication of bulk and nano devices Each chapter of this proposed title starts with a brief introduction of the presented topic and ends with a summary indicating the futuristic aspect including practice questions Aimed at researchers and senior undergraduate graduate students in electrical and electronics engineering microelectronics nanoelectronics and nanotechnology this book Provides broad and comprehensive coverage from Microelectronics to Nanoelectronics including design in analog and digital electronics Includes HDL and VLSI design going into the nanoelectronics arena Discusses devices circuit analysis design methodology and real time simulation based on industry standard HSPICE tool Explores emerging devices such as FinFETs Tunnel FETs TFETs and CNTFETs including their circuit

co designing Covers real time illustration using industry standard Verilog Cadence and Synopsys simulations *Applications of Zero-Suppressed Decision Diagrams* Jon T. Butler, Tsutomu Sasao, 2022-05-31 A zero suppressed decision diagram ZDD is a data structure to represent objects that typically contain many zeros Applications include combinatorial problems such as graphs circuits faults and data mining This book consists of four chapters on the applications of ZDDs The first chapter by Alan Mishchenko introduces the ZDD It compares ZDDs to BDDs showing why a more compact representation is usually achieved in a ZDD The focus is on sets of subsets and on sum of products SOP expressions Methods to generate all the prime implicants PIs and to generate irredundant SOPs are shown A list of papers on the applications of ZDDs is also presented In the appendix ZDD procedures in the CUDD package are described The second chapter by Tsutomu Sasao shows methods to generate PIs and irredundant SOPs using a divide and conquer method This chapter helps the reader to understand the methods presented in the first chapter The third chapter by Shin Ichi Minato introduces the frontier based method that efficiently enumerates certain subsets of a graph The final chapter by Shinobu Nagayama shows a method to match strings of characters This is important in routers for example where one must match the address information of an internet packet to the proper output port It shows that ZDDs are more compact than BDDs in solving this important problem Each chapter contains exercises and the appendix contains their solutions Table of Contents Preface Acknowledgments Introduction to Zero Suppressed Decision Diagrams Efficient Generation of Prime Implicants and Irredundant Sum of Products Expressions The Power of Enumeration BDD ZDD Based Algorithms for Tackling Combinatorial Explosion Regular Expression Matching Using Zero Suppressed Decision Diagrams Authors and Editors Biographies Index

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