

Digital Computer Structure And Design

David J. Lilja, Sachin S. Sapatnekar

Digital Computer Structure And Design:

Digital Computer Structure and Design R. Townsend, 2014-05-20 Digital Computer Structure and Design Second Edition discusses switching theory counters sequential circuits number representation and arithmetic functions The book also describes computer memories the processor data flow system of the processor the processor control system and the input output system Switching theory which is purely a mathematical concept centers on the properties of interconnected networks of gates The theory deals with binary functions of 1 and 0 which can change instantaneously from one to the other without intermediate values The binary number system is used in computer arithmetic and other operations due to its simplicity that can be easily adopted in device parameters These operations involve only two levels the on or off positions which also offer maximum immunity to noise or circuit interference The binary system is a very efficient way to represent numbers or to store data When the computer uses this system the clock cycle of the processor determines or divides the cycles for each sub operation into steps A master timing counter defines each of these steps and synchronizes them avoid data loss or mix ups After the sub operation has been completed the monitor will display the result Programmers computer engineers computer instructors and students of computer science will find the book highly useful Digital Design and Computer Architecture David Harris, Sarah Harris, 2012-08-24 Digital Design and Computer Architecture Second Edition takes a unique and modern approach to digital design introducing the reader to the fundamentals of digital logic and then showing step by step how to build a MIPS microprocessor in both Verilog and VHDL This new edition combines an engaging and humorous writing style with an updated and hands on approach to digital design It presents new content on I O systems in the context of general purpose processors found in a PC as well as microcontrollers found almost everywhere Beginning with digital logic gates and progressing to the design of combinational and sequential circuits the book uses these fundamental building blocks as the basis for the design of an actual MIPS processor It provides practical examples of how to interface with peripherals using RS232 SPI motor control interrupts wireless and analog to digital conversion SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design There are also additional exercises and new examples of parallel and advanced architectures practical I O applications embedded systems and heterogeneous computing plus a new appendix on C programming to strengthen the connection between programming and processor architecture This new edition will appeal to professional computer engineers and to students taking a course that combines digital logic and computer architecture Updated based on instructor feedback with more exercises and new examples of parallel and advanced architectures practical I O applications embedded systems and heterogeneous computing Presents digital system design examples in both VHDL and SystemVerilog updated for the second edition from Verilog shown side by side to compare and contrast their strengths Includes a new chapter on C programming to provide necessary prerequisites and strengthen the connection between programming and processor architecture

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Computer Architecture David Money Harris, Sarah L. Harris, 2013 Provides practical examples of how to interface with peripherals using RS232 SPI motor control interrupts wireless and analog to digital conversion This book covers the fundamentals of digital logic design and reinforces logic concepts through the design of a MIPS microprocessor ELECTRONICS, COMPUTER ARCHITECTURE AND MICROPORCESSOR DESIGN PRINCIPLES: WITH REAL LIFE PRACTICAL APPLICATION IN COMPUTING, NETWORKING, MINING, REMOTE SENSING, DATABASE AND IMAGERY Jagdish Krishanlal Arora, Table of Contents CHAPTER 1 MICROPROCESSOR CHAPTER 2 SILICON WAFERS CHIPS CHAPTER 3 TRANSISTORS CHAPTER 4 LOGIC GATES CHAPTER 5 BOOLEAN ALGEBRA AND STORING NUMBERS CHAPTER 6 BINARY CONVERSION OF TEXT AUDIO IMAGE AND VIDEO CHAPTER 7 DATA COMPRESSION CHAPTER 8 REGISTERS CHAPTER 9 THE CONTROL UNIT CHAPTER 10 ARITHMETIC LOGIC UNIT ALU CHAPTER 11 DATA PATHS AND MULTIPLEXERS CHAPTER 12 BIOS Basic Input Output System CHAPTER 13 ASSEMBLY LANGUAGE CHAPTER 14 HARD DISK CHAPTER 15 RAM AND ROM CHAPTER 16 DIFFERENT TYPES OF MICROPROCESSORS CHAPTER 17 ASIC Application Specific Integrated Circuit CHAPTER 18 FPGA Field Programmable Gate Array CHAPTER 19 PRISM Parallel Reduced Instruction Set Multiprocessor CHAPTER 20 COMPUTER MOTHERBOARDS CHAPTER 21 WIRELESS COMMUNICATION CHAPTER 22 KEYBOARD AND MOUSE CHAPTER 23 ROUTER AND SWITCHES CHAPTER 24 OPERATING SYSTEM CHAPTER 25 Project DESIGNING A 4 BIT MICROPROCESSOR CHAPTER 26 ROBOTICS CHAPTER 27 ARTIFICAL INTELLIGENCE CHAPTER 28 NETWORKING CHAPTER 29 CLOUD COMPUTING AND CLOUD STORAGE CHAPTER 30 DATABASES CHAPTER 31 BLOCK CHAIN CRYPTOCURRENCY AND MINING CHAPTER 32 REMOTE Theory and Design of Digital Computer Systems Douglas Lewin, David Noaks, 1992-11-30 A second and third SENSING year text for courses in computer engineering and digital systems this work is concerned with the way in which the computer in its simplest form is a component in a system which can be used to construct complex digital architectures Design and Computer Architecture, RISC-V Edition Sarah Harris, David Harris, 2021-07-12 The newest addition to the Harris and Harris family of Digital Design and Computer Architecture books this RISC V Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Combining an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of a processor By the end of this book readers will be able to build their own RISC V microprocessor and will have a top to bottom understanding of how it works Beginning with digital logic gates and progressing to the design of combinational and sequential circuits this book uses these fundamental building blocks as the basis for designing a RISC V processor SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design The companion website includes a chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs

Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Gives students a full understanding of the RISC V instruction set architecture enabling them to build a RISC V processor and program the RISC V processor in hardware simulation software simulation and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single cycle multicycle and pipelined versions of the RISC V architecture Features a companion website with a bonus chapter on I O systems with practical examples that show how to use SparkFun's RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive problems Digital Landscape Architecture: Logic, Structure, Method and Application Yuning Cheng, 2023-05-13 Closely related to the frontier research field of digital technology this book reshapes the planning and design process of landscape architecture from theoretical and practical levels It gives a full scale discussion to the logic structure method and application of digital landscape architecture leading this field to a new era of perception quantification research mode Readers will get a comprehensive understanding of digital landscape architecture know about multiple digital methods for landscape planning and design and learn a lot of practical projects with digital technology And it will inspire the readers to think about new patterns and approaches to landscape planning rather than traditional ways This book is organized under a clear logic which helps the readers easily get the core of the work A lot of logic diagrams showing between the theoretical paragraphs highly summarize the key points of the book providing a better readability and acceptability This book also contains many detailed drawings and graphics for the project cases which gives a good demonstration of how digital methods could be applied in practice **Digital Design and Computer Architecture** David Money Harris, Sarah L. Harris, 2015 **Principles of Digital Computer Design** Abd-Elfattah M. Abd-Alla, Arnold C. Meltzer, 1976 Computer uses and application Number systems Arithmetic operations Codes Switching algebra and logic gates Functional logic subunits Computer architecture and programming Arithmetic units Memory Data input output and channels The control unit Design of a small digital computer Computer System Architecture M. Morris Mano, 1993 Dealing with computer architecture as well as computer organization and design this fully updated book provides the basic knowledge necessary to understand the hardware operation of digital computers Written to aid electrical engineers computer engineers and computer scientists the volume includes KEY FEATURES the computer architecture organization and design associated with computer hardware the various digital components used in the organization and design of digital computers detailed steps that a designer must go through in order to design an elementary basic computer the organization and

architecture of the central processing unit the organization and architecture of input output and memory the concept of multiprocessing two new chapters on pipeline and vector processing two sections devoted completely to the reduced instruction set computer RISC and sample worked out problems to clarify topics Computer Systems Ata Elahi, 2022 This updated textbook covers digital design fundamentals of computer architecture and ARM assembly language The book starts by introducing computer abstraction basic number systems character coding basic knowledge in digital design and components of a computer The book goes on to discuss information representation in computing Boolean algebra and logic gates and sequential logic The book also presents introduction to computer architecture Cache mapping methods and virtual memory The author also covers ARM architecture ARM instructions ARM assembly language using Keil development tools and bitwise control structure using C and ARM assembly language The book includes a set of laboratory experiments related to digital design using Logisim software and ARM assembly language programming using Keil development tools In addition each chapter features objectives summaries key terms review questions and problems **Digital Design and Computer Organization** Hassan A. Farhat, 2003-12-29 Digital Design and Computer Organization introduces digital design as it applies to the creation of computer systems It summarizes the tools of logic design and their mathematical basis along with in depth coverage of combinational and sequential circuits The book includes an accompanying CD that includes the majority of circuits highlighted in the text delivering you hands on experience in the simulation and observation of circuit functionality These circuits were designed and tested with a user friendly Electronics Workbench package Multisim Textbook Edition that enables your progression from truth tables onward to more complex designs This volume differs from traditional digital design texts by providing a complete design of an AC based CPU allowing you to apply digital design directly to computer architecture The book makes minimal reference to electrical properties and is vendor independent allowing emphasis on the general design principles Digital Computer Technology and Design: Circuits and machine design Willis H. **Computer Systems** Ata Elahi, 2017-11-08 This textbook covers digital design fundamentals of computer Ware, 1963 architecture and assembly language The book starts by introducing basic number systems character coding basic knowledge in digital design and components of a computer The book goes on to discuss information representation in computing Boolean algebra and logic gates sequential logic input output and CPU performance The author also covers ARM architecture ARM instructions and ARM assembly language which is used in a variety of devices such as cell phones digital TV automobiles routers and switches The book contains a set of laboratory experiments related to digital design using Logisim software in addition each chapter features objectives summaries key terms review questions and problems The book is targeted to students majoring Computer Science Information System and IT and follows the ACM IEEE 2013 guidelines Comprehensive textbook covering digital design computer architecture and ARM architecture and assembly Covers basic number system and coding basic knowledge in digital design and components of a computer Features laboratory exercises in

addition to objectives summaries key terms review questions and problems in each chapter Designing Digital Computing Systems with Verilog David J. Lilja, Sachin S. Sapatnekar, 2005 **Computer Architecture and Organization Miles I.** Murdocca, Vincent P. Heuring, 2007-03-16 An accessible introduction to computer systems and architecture Anyone aspiring to more advanced studies in computer related fields must gain an understanding of the two parallel aspects of the modern digital computer programming methodology and the underlying machine architecture The uniquely integrated approach of Computer Architecture and Organization connects the programmer's view of a computer system with the associated hardware and peripheral devices providing a thorough three dimensional view of what goes on inside the machine Covering all the major topics normally found in a first course in computer architecture the text focuses on the essentials including the instruction set architecture ISA network related issues and programming methodology Using real world case studies to put the information into perspective the chapters examine Data representation Arithmetic The instruction set architecture Datapath and Control Languages and the machine Memory Buses and peripherals Networking and communication Advanced computer architecture A valuable feature of this book is the use of ARC a subset of the SPARC processor for an instruction set architecture A platform independent ARCTools suite containing an assembler and simulator for the ARC ISA that supports the examples used in the book is available Better yet the content is supplemented by online problem sets available through WileyPlus Balanced and thoughtfully designed for use as either a classroom text or self study guide Computer Architecture and Organization An Integrated Approach will put you solidly on track for advancing to higher levels in computer related disciplines About the Author MILES MURDOCCAserves as the President and CEO of Internet Institute USA IIUSA a private postsecondary information technology IT school specializing in networking operating systems IP telephony programming and security Previously Dr Murdocca has been a computer science faculty member at Rutgers University and a research scientist at AT T Bell Laboratories working in computer architecture networking and digital optical computing He is the author of A Digital Design Methodology for Optical Computing and Principles of Computer Architecture and a contributing author to Computer Systems Design and Architecture Second Edition as well as the author of dozens of professional papers and patents relating to information technology VINCE HEURING is an associate professor and acting chair of the Department of Electrical and Computer Engineering at the University of Colorado at Boulder He has been at the university since 1984 and prior to that he spent three years at the University of Cincinnati Professor Heuring's research encompasses computer architectures and programming language design implementation He and his colleague Harry Jordan designed and built the world s first stored program optical computer SPOC

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