Compilers and Operating Systems for Low Power

Compilers And Operating Systems For Low Power

RD Boyd

Compilers And Operating Systems For Low Power:

  Compilers and Operating Systems for Low Power Luca Benini,Mahmut Kandemir,J. Ramanujam,2011-06-28 Compilers and Operating Systems for Low Power focuses on both application level compiler directed energy optimization and low power operating systems Chapters have been written exclusively for this volume by several of the leading researchers and application developers active in the field The first six chapters focus on low energy operating systems or more in general energy aware middleware services The next five chapters are centered on compilation and code optimization Finally the last chapter takes a more general viewpoint on mobile computing The material demonstrates the state of the art work and proves that to obtain the best energy performance characteristics compilers system software and architecture must work together The relationship between energy aware middleware and wireless microsensors mobile computing and other wireless applications are covered This work will be of interest to researchers in the areas of low power computing embedded systems compiler optimizations and operating systems *Power-Aware Computer Systems* Babak Falsafi,2004-12-17 Welcome to the proceedings of the 3rd Power Aware Computer Systems PACS 2003 Workshop held in conjunction with the 36th Annual International Symposium on Microarchitecture MICRO 36 The increase in power and ergy dissipation in computer systems has begun to limit performance and has also resulted in higher cost and lower reliability The increase also implies ducedbatterylifeinportablesystems Becauseofthemagnitudeoftheproblem alllevelsofcomputersystems includingcircuits architectures andsoftware are being employed to address power and energy issues PACS 2003 was the third workshop in its series to explore power and energy awareness at all levels of computer systems and brought together experts from academia and industry These proceedings include 14 research papers selected from 43 submissions spanningawidespectrumofareasinpower awaresystems Wehavegrouped the papers into the following categories 1 compilers 2 embedded systems 3 microarchitectures and 4 cache and memory systems The rst paper on compiler techniques proposes pointer reuse analysis that is biased by runtime information i e the targets of pointers are determined based on the likelihood of their occurrence at runtime to map accesses to ener e cient memory access paths e g avoid tag match Another paper proposes compiling multiple programs together so that disk accesses across the programs can be synchronized to achieve longer sleep times in disks than if the programs are optimized separately *Power-efficient System Design* Preeti Ranjan Panda,B. V. N. Silpa,Aviral Shrivastava,Krishnaiah Gummidipudi,2010-07-23 The Information and communication technology ICT industry is said to account for 2% of the worldwide carbon emissions a fraction that continues to grow with the relentless push for more and more sophisticated computing equipment c munications infrastructure and mobile devices While computers evolved in the directionofhigherandhigherperformanceformostofthelatterhalfofthe20thc tury the late 1990 s and early 2000 ssaw a new emergingfundamentalconcern that has begun to shape our day to day thinking in system design power dissipation As we elaborate in Chapter 1 a variety of factors colluded to raise power ef ciency as a rst class design concern in

the designer s mind with profound consequences all over the eld semiconductor process design circuit design design automation tools system and application software all the way to large data centers Power ef cient System Design originated from a desire to capture and highlight the exciting developments in the rapidly evolving eld of power and energy op mization in electronic and computer based systems Tremendous progress has been made in the last two decades and the topic continues to be a fascinating research area To develop a clearer focus we have concentrated on the relatively higher level of design abstraction that is loosely called the system level In addition to the ext sive coverage of traditional power reduction targets such as CPU and memory the book is distinguished by detailed coverage of relatively modern power optimization ideas focussing on components such as compilers operating systems servers data centers and graphics processors

Compiler Construction Shriram Krishnamurthi,Martin Odersky,2007-07-02 This book constitutes the refereed proceedings of the 16th International Conference on Compiler Construction CC 2007 held in Braga Portugal in March 2007 as part of ETAPS 2007 the European Joint Conferences on Theory and Practice of Software The 15 revised full are organized in topical sections on architecture garbage collection and program analysis register allocation and program analysis

Proceedings of the ... ACM SIGPLAN Symposium on Principles & Practice of Parallel Programming ,2006

Power-aware Computer Systems ,2004   The Compiler Design Handbook Y.N. Srikant,Priti Shankar,2018-10-03 Today s embedded devices and sensor networks are becoming more and more sophisticated requiring more efficient and highly flexible compilers Engineers are discovering that many of the compilers in use today are ill suited to meet the demands of more advanced computer architectures Updated to include the latest techniques The Compiler Design Handbook Second Edition offers a unique opportunity for designers and researchers to update their knowledge refine their skills and prepare for emerging innovations The completely revised handbook includes 14 new chapters addressing topics such as worst case execution time estimation garbage collection and energy aware compilation The editors take special care to consider the growing proliferation of embedded devices as well as the need for efficient techniques to debug faulty code New contributors provide additional insight to chapters on register allocation software pipelining instruction scheduling and type systems Written by top researchers and designers from around the world The Compiler Design Handbook Second Edition gives designers the opportunity to incorporate and develop innovative techniques for optimization and code generation

Proceedings of the USENIX ... Annual Technical Conference ,2003   *Transactions on High-Performance Embedded Architectures and Compilers II* Per Stenström,David Whalley,2009-04-22 This book contains extended versions of key papers from the 2nd International Conference on High Performance Embedded Architectures and Compilers HiPEAC 2007 It also covers such topics as microarchitecture code generation and performance modeling   **2004 International Conference on Supercomputing** ,2004   *Proceedings* ,2004   ACM SIGPLAN Notices ,2006-07   **Proceedings of the general track : 2003 USENIX annual technical conference : June 9 - 14, 2003, San Antonio, Texas, USA**

USENIX Association,2003   *Integrated Circuit and System Design* ,2005   <u>PACT 2002</u> IEEE Computer Society. Technical Committee on Computer Architecture,2002 Consists of 25 papers and three talks from the September 2002 conference on parallelism and compilers Several of the papers address the related subjects of memory systems energy consumption and software translation Among the topics are resource sharing in SMT processors for high single thread pe   *Proceedings of the 36th Annual IEEE/ACM International Symposium on Microarchitecture* ,2003 MICRO 36 discusses new microarchitecture and software techniques and explores the latest research related to processor architecture compilers and systems for technical interaction on traditional MICRO topics The proceedings also places a special emphasis on optimizations that take advantage of application specific opportunities In addition this year s MICRO includes papers that will benefit both the microarchitecture and embedded architecture communities   *Annales des télécommunications* ,2004-07   *Chinese Journal of Electronics* ,2004   *Languages, Compilers, and Tools for Embedded Systems* ,2000   <u>Embedded Software</u> ,2006

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