

DYNAMIC CHARACTERISATION OF ANALOGUE-TO-DIGITAL CONVERTERS

Dominique Dallet and
José Machado da Silva (Eds.)
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Dynamic Characterisation Of Analogue To Digital Converters

David Michael Thomasson



Dynamic Characterisation Of Analogue To Digital Converters:

Dynamic Characterisation of Analogue-to-Digital Converters Dominique Dallet, José Machado da Silva, 2006-03-08 The Analogue to digital converter ADC is the most pervasive block in electronic systems With the advent of powerful digital signal processing and digital communication techniques ADCs are fast becoming critical components for system s performance and flexibility Knowing accurately all the parameters that characterise their dynamic behaviour is crucial on one hand to select the most adequate ADC architecture and characteristics for each end application and on the other hand to understand how they affect performance bottlenecks in the signal processing chain *Dynamic Characterisation of Analogue to Digital Converters* presents a state of the art overview of the methods and procedures employed for characterising ADCs dynamic performance behaviour using sinusoidal stimuli The three classical methods histogram sine wave fitting and spectral analysis are thoroughly described and new approaches are proposed to circumvent some of their limitations This is a must have compendium which can be used by both academics and test professionals to understand the fundamental mathematics underlining the algorithms of ADC testing and as an handbook to help the engineer in the most important and critical details for their implementation

Dynamic Characterisation of Analogue-To-Digital Converters Stanley Lawrence, 2017-05-09 The Analogue to digital converter ADC is the most pervasive block in electronic systems With the advent of powerful digital signal processing and digital communication techniques ADCs are fast becoming critical components for system s performance and flexibility Knowing accurately all the parameters that characterise their dynamic behaviour is crucial on one hand to select the most adequate ADC architecture and characteristics for each end application and on the other hand to understand how they affect performance bottlenecks in the signal processing chain

Analog Circuit Design Techniques at 0.5V Shouri Chatterjee, K.P. Pun, Nebojša Stanic, Yannis Tsiividis, Peter Kinget, 2010-04-02 Analog design at ultra low supply voltages is an important challenge for the semiconductor research community and industry *Analog Circuit Design Techniques at 0.5V* covers challenges for the design of MOS analog and RF circuits at a 0.5 V power supply voltage All design techniques presented are true low voltage techniques all nodes in the circuits are within the power supply rails The circuit implementations of body and gate input fully differential amplifiers are also discussed These building blocks enable us to build continuous time filters track and hold circuits and continuous time sigma delta modulators Current books on low voltage analog design typically cover techniques for supply voltages down to approximately 1V This book presents novel ideas and results for operation from much lower supply voltages and the techniques presented are basic circuit techniques that are widely applicable beyond the scope of the presented examples *Analog Circuit Design Techniques at 0.5V* is written for analog circuit designers and researchers as well as graduate students studying semiconductors and integrated circuit design

Sigma Delta A/D Conversion for Signal Conditioning Kathleen Philips, Arthur H.M. van Roermund, 2006-05-05

1.1 Background Moore s Law predicts a decrease by a factor of two in the feature size of CMOS technology every three years

and has been valid for years It implies a doubling of the era tion speed and a four times higher transistor count per unit of area every three years The combination leads to an eight times higher processing capability per unit of area This on going miniaturization allows the integration of complex electronic systems with millions of transistors Very Large Scale Integration and enables the integration of el tronic systems An electronic system A generic picture of an integrated electronic system is shown in g 1 1 The heart of the system is the signal processing core This core supports a wide variety of functions such as customization and programmability of multiple applications channel coding the de nition of the user interface etc These functions are enabled by DSP a controller CPU and various blocks of memory In advanced ICs these blocks provide almost all signal processing and usually dominate in the overall power and area consumption of integrated systems The huge data rates involved require high speed busses for communication between these blocks A power management unit fuels the system by providing the propriate supply voltages and currents

CMOS Cascade Sigma-Delta Modulators for Sensors and

Telecom Rocío Río Fernández,Fernando Medeiro Hidalgo,Belén Pérez Verdú,José Manuel Rosa Utrera,Ángel Rodríguez-Vázquez,2006-09-03 Institutional book not really for bookstore catalogue The book contains valuable information structured to provide insight on how to design SC sigma delta modulators It presents architectures circuits models methods and practical considerations for the design of high performance low pass switched capacitor SC sigma delta A D interfaces for mixed signal CMOS ASICs The main focus of the book is on cascade architectures It differs from other books in the complete in depth coverage of SC circuit errors

High-Speed Photodiodes in Standard CMOS Technology Sasa Radovanovic,Anne-Johan Annema,Bram Nauta,2006-10-11 High speed Photodiodes in Standard CMOS Technology describes high speed photodiodes in standard CMOS technology which allow monolithic integration of optical receivers for short haul communication For short haul communication the cost aspect is important and therefore it is desirable that the optical receiver can be integrated in the same CMOS technology as the rest of the system If this is possible then ultimately a singe chip system including optical inputs becomes feasible eliminating EMC and crosstalk problems while data rate can be extremely high The problem of photodiodes in standard CMOS technology it that they have very limited bandwidth allowing data rates up to only 50Mbit per second High speed Photodiodes in Standard CMOS Technology first analyzes the photodiode behaviour and compares existing solutions to enhance the speed After this the book introduces a new and robust electronic equalizer technique that makes data rates of 3Gb s possible without changing the manufacturing technology The application of this technique can be found in short haul fibre communication optical printed circuit boards but also photodiodes for laser disks

Low-Power Low-Voltage Sigma-Delta Modulators in Nanometer CMOS Libin Yao,Michiel Steyaert,Willy M. C. Sansen,2006-02-06 this book is not suitable for the bookstore catalogue

Switched-Capacitor Techniques for High-Accuracy Filter and ADC Design Patrick J. Quinn,Arthur H.M. van Roermund,2007-07-20 This book proposes alternative switched capacitor techniques which allow the achievement of higher intrinsic analogue functional accuracy than previously

possible in such application areas as analogue filter and ADC design The validity of the concepts developed and analyzed in Switched Capacitor Techniques for High Accuracy Filter and ADC Design has been demonstrated in practice with the design of CMOS SC bandpass filters and algorithmic ADC stages **Power Quality** Mircea Ion Buzdugan, Manuel Perez

Donsion, 2017-01-06 Power quality is a very broad subject covering all stages of power systems engineering from the generation transmission and distribution levels to the end users This book contains a selection of the best papers on power quality presented at the International Conferences on Renewable Energy and Power Quality from 2003 to 2012 The volume represents a unique selection of the best contributions to power quality exploitation and evolution over the past decade As such it provides an up to date reference point for researchers technicians and engineering interested in the state of the field of power quality This book will primarily interest professional engineers and researchers dealing with power quality but will also prove useful to postgraduate level students It can also be used as a reference book for engineers physicists and mathematicians interested and involved in operation project management design and analysis of power quality issues Each chapter contains references that allow the treated topic to be further deepened **Proceedings of the ... IEEE**

International Conference on Electronics, Circuits, and Systems, 1998 Direct Digital Control Systems James Brian Knowles, 1994 Relates rigorously derived mathematical relationships for single input DDC systems to their practical design Deterministic and stochastic input data are considered with the objective of demonstrating the close mathematical similarities between results derived by the z and Laplace Transformations *Video-rate Analog-to-digital Conversion Using Pipelined Architectures* Stephen Henry Lewis, 1987 *The Circuits and Filters Handbook* Wai-Kai Chen, 1995-06-29 This invaluable reference book features the most comprehensive coverage ever of circuits and filters from classical to state of the art designs It begins with a discussion of basic mathematics for signal processing and circuit and filter design then goes on to investigate the underlying theory and applications including a thorough analysis of both analog and digital circuits and filters

The Static and Dynamic Characterization of the MC68HC11A8's Analog to Digital Converter Jeffrey Charles Daniels, 1988

Experimental Modal Analysis and Dynamic Component Synthesis: Measurement techniques for experimental modal analysis Randall J. Allemang, 1987 *IEEE Instrumentation and Measurement Technology Conference*, 1999 IEEE Instrumentation and Measurement Society, 1999 The conference will cover all aspects of theory and practice of metrology measurement technologies instrumentation and related applications *IEEE Instrumentation and Measurement Technology Conference*, 1992 **Development and Initial Characterization of a Nuclear Magnetic Resonance Dosimetry System** David Michael Thomasson, 1989 **Conference Publication**, 1991 *Experimental Modal Analysis and Dynamic Component Synthesis: Summary of technical work* Randall J. Allemang, 1987

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