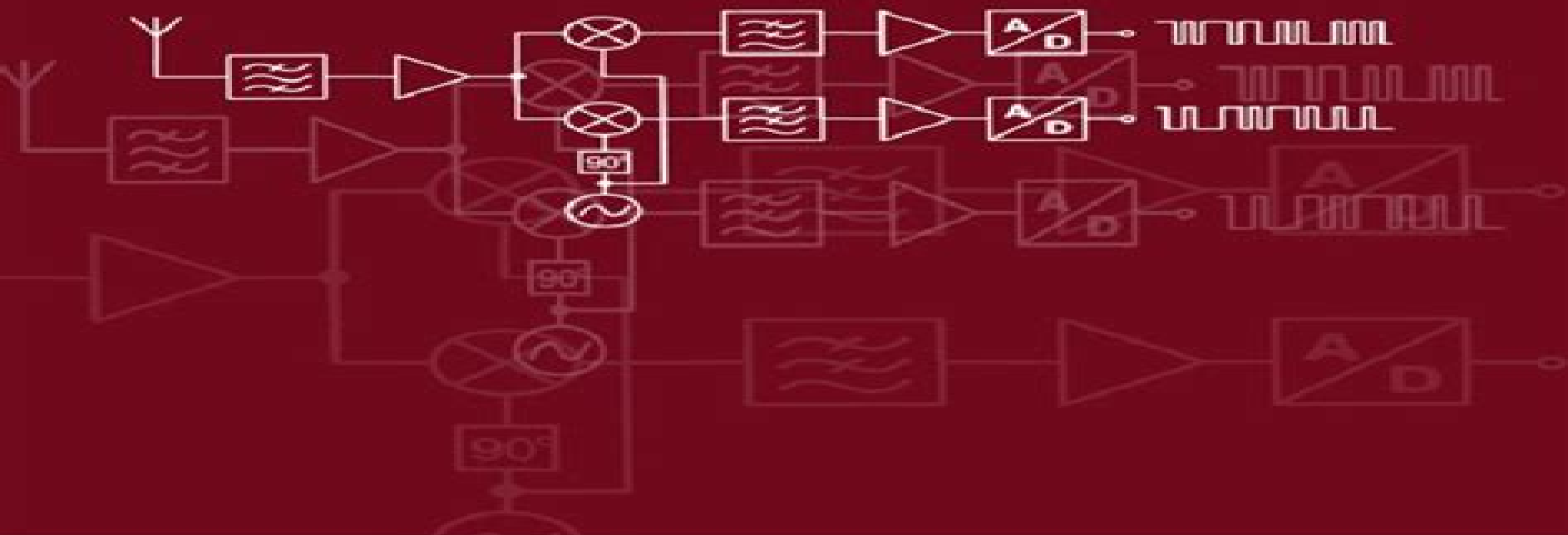


DIRECT CONVERSION RECEIVERS IN WIDE-BAND SYSTEMS

Aarno Pärssinen



Direct Conversion Receivers In Wide Band Systems

**Asad A. Abidi, Paul R. Gray, Robert G.
Meyer**



Direct Conversion Receivers In Wide Band Systems:

Direct Conversion Receivers in Wide-Band Systems Aarno Pärssinen, 2001-12-31 Wide band systems will be the next significant generation in wireless communications. Those include both wireless local area networks and cellular systems with a large coverage area. They will provide a higher data rate and access to internet and video services for example. Although most of the data processing is performed digitally, also the requirements and possibilities to implement the analog part of the radio receiver will be different compared to the second generation narrow band receivers. Direct conversion architecture is a distinct candidate for wide band systems because some non idealities involved in baseband signal processing are significantly relaxed. The requirements and feasibility of direct conversion in wide band systems are analyzed in this work. The main emphasis is on cellular systems based on direct sequence code division multiple access but the same principles are generally valid in all receivers for different applications. The basic principles and design methods involved in receiver design are overviewed as well as the most common radio architectures. In a detailed analysis the fundamental limitations of the direct conversion architecture are analyzed in wide band signal processing. Especially the effect of envelope distortion is characterized both with respect to the specific modulation and to the implementation of a downconversion mixer. Downconversion mixer is the key component in direct conversion because it transfers the radio frequency signal immediately down into the baseband after a relatively small gain at the preceding signal processing blocks which do not provide filtering of the unwanted radio channels within the system band. Both switching mixers and subsampling mixers are analyzed. Direct Conversion Receivers in Wide Band Systems consists of four different circuit implementations. A subharmonic sampler operating up to 2 GHz is implemented with a GaAs MESFET technology. The second IC is a CMOS low noise amplifier with an optimized interface to a subsampling mixer. Two BiCMOS implementations of the wide band direct conversion receiver are given. The first consists of four different chips: RF front end, analog baseband circuitry and two analog to digital converters. In the second chip all blocks from the low noise amplifier to the A/D converters are placed on the same die. In that case an excellent isolation is required between rail to rail clock signals and the sensitive RF input.

Direct Conversion Receivers in Wide-Band Systems Aarno Pärssinen, 2006-04-18 This book is based on my doctoral thesis at the Helsinki University of Technology. Several different projects during five years guided me from the basics of the RF IC design to the implementations of highly integrated radio receiver chips. Sharing time and effort between IC and system issues is not always straightforward. I have been lucky to follow both topics and share experiences with diligent and enthusiastic people having different specialities. As a result this book will cover a wide range of different topics needed in the design of highly integrated radio receivers. Experiences from the first receiver prototypes for the third generation cellular systems form the basis of this book. Most of the issues are directly related to the early proposals of European and Japanese standardization organizations. For example the chip rate was originally set to 4 096 Mcps in a wide band CDMA channel. I have kept that number in the book in

most of the examples although it has been later changed to 384 Mcps I hope that the readers will accept that and the possible other incompatibilities to the latest specifications At least in the research phase the changes even in the most essential requirements are definitely not a rare incident and IC designers should be able to react and modify their designs as soon as they can

Continuous-Time Low-Pass Filters for Integrated Wideband Radio Receivers Ville Saari, Jussi Ryyänen, Saska Lindfors, 2012-03-15 This book presents a new filter design approach and concentrates on the circuit techniques that can be utilized when designing continuous time low pass filters in modern ultra deep submicron CMOS technologies for integrated wideband radio receivers Coverage includes system level issues related to the design and implementation of a complete single chip radio receiver and related to the design and implementation of a filter circuit as a part of a complete single chip radio receiver Presents a new filter design approach emphasizing low voltage circuit solutions that can be implemented in modern ultra deep submicron CMOS technologies Includes filter circuit implementations designed as a part of a single chip radio receiver in modern 1.2V 0.13μm and 65nm CMOS Describes design and implementation of a continuous time low pass filter for a multicarrier WCDMA base station Emphasizes system level considerations throughout

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ASICON 2003 Ting-Ao Tang, 2003

Circuits for Wireless Communications Banlue Srisuchinwong, Wanlop Surakamponporn, Sawasdtantaratana, 2001 Wireless communications is one of the fastest growing areas of communications technology This book presents current and classic papers on circuits for wireless communications Selected by three experts in the field the text should be useful for students engineers just beginning to work in cellular communications and experienced engineers who want a convenient and useful reference source

Proceedings of the ... Midwest Symposium on Circuits and Systems, 2003

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Integrated Circuits for Wireless Communications Asad A. Abidi, Paul R. Gray, Robert G. Meyer, 1998-12-24 Electrical Engineering Integrated Circuits for Wireless Communications High frequency integrated circuit design is a booming area of growth that is driven not only by the expanding capabilities of underlying circuit technologies like CMOS but also by the dramatic increase in wireless communications products that depend on them Integrated Circuits for Wireless Communications includes seminal and classic papers in the field and is the first all in one resource to address this increasingly important topic Internationally known and highly regarded in the field editors Asad Abidi Paul Gray and Robert G Meyer have meticulously compiled more than 100 papers and articles covering the very latest high level integrated circuits techniques and solutions in use today Integrated Circuits for Wireless Communications is devised expressly to provide IC design engineers system architects and integrators with a practical understanding of subjects ranging from architecture choices for integrated transceivers to actual circuit designs in all viable IC technologies such as bipolar CMOS and GaAs The

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