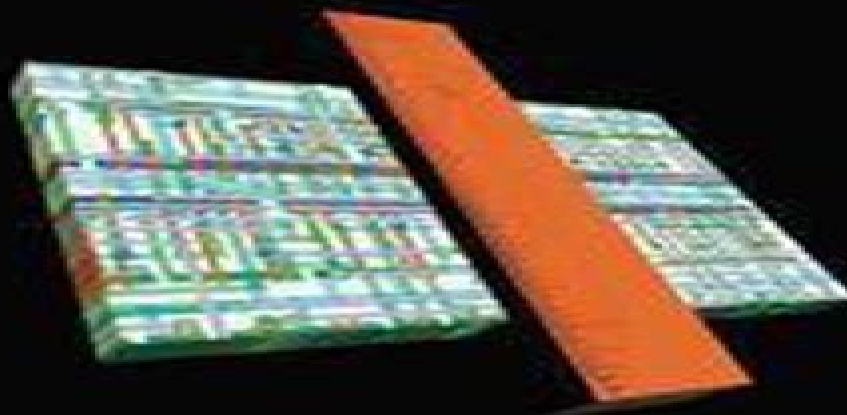


Direct Transistor-Level Layout For Digital Blocks

Prakash Gopalakrishnan
Rob A. Rutenbar



Direct Transistorlevel Layout For Digital Blocks

**Institut national polytechnique
(Grenoble). Club conception de
circuits à la demande**



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Direct Transistor-Level Layout for Digital Blocks Prakash Gopalakrishnan, Rob A. Rutenbar, 2006-01-16 Cell based design methodologies have dominated layout generation of digital circuits Unfortunately the growing demands for transparent process portability increased performance and low level device sizing for timing power are poorly handled in a fixed cell library Direct Transistor Level Layout For Digital Blocks proposes a direct transistor level layout approach for small blocks of custom digital logic as an alternative that better accommodates demands for device level flexibility This approach captures essential shape level optimizations yet scales easily to netlists with thousands of devices and incorporates timing optimization during layout The key idea is early identification of essential diffusion merged MOS device groups and their preservation in an uncommitted geometric form until the very end of detailed placement Roughly speaking essential groups are extracted early from the transistor level netlist placed globally optimized locally and then finally committed each to a specific shape level form while concurrently optimizing for both density and routability The essential flaw in prior efforts is an over reliance on geometric assumptions from large scale cell based layout algorithms Individual transistors may seem simple but they do not pack as gates do Algorithms that ignore these shape level issues suffer the consequences when thousands of devices are poorly packed The approach described in this book can pack devices much more densely than a typical cell based layout Direct Transistor Level Layout For Digital Blocks is a comprehensive reference work on device level layout optimization which will be valuable to CAD tool and circuit designers

Direct Transistor-level Layout For Digital Blocks Prakash Gopalakrishnan, Rob A. Rutenbar, 2004-05-01 Modeling and Simulation for RF System Design Ronny Frevert, Joachim Haase, Roland Jancke, Uwe Knochel, Peter Schwarz, Ralf Kakerow, Mohsen Darianian, 2006-06-28 Modern telecommunication systems are highly complex from an algorithmic point of view The complexity continues to increase due to advanced modulation schemes multiple protocols and standards as well as additional functionality such as personal organizers or navigation aids To have short and reliable design cycles efficient verification methods and tools are necessary Modeling and simulation need to accompany the design steps from the specification to the overall system verification in order to bridge the gaps between system specification system simulation and circuit level simulation Very high carrier frequencies together with long observation periods result in extremely large computation times and requires therefore specialized modeling methods and simulation tools on all design levels The focus of Modeling and Simulation for RF System Design lies on RF specific modeling and simulation methods and the consideration of system and circuit level descriptions It contains application oriented training material for RF designers which combines the presentation of a mixed signal design flow an introduction into the powerful standardized hardware description languages VHDL AMS and Verilog A and the application of commercially available simulators Modeling and Simulation for RF System Design is addressed to graduate students and industrial professionals who are engaged in communication system design and want to gain insight into the system structure

by own simulation experiences The authors are experts in design modeling and simulation of communication systems engaged at the Nokia Research Center Bochum Germany and the Fraunhofer Institute for Integrated Circuits Branch Lab Design Automation Dresden Germany **Proceedings of Technical Papers** ,2005 **Radiation Tolerant Nyquist Analog to Digital Converters** Zheyi Li,Laurent Berti,Paul Leroux,2025-09-30 This book presents the detailed design considerations and techniques for radiation tolerant RT Nyquist analog to digital converters ADC It begins with the fundamental radiation effects in space and its consequences in modern CMOS technology Next radiation effects on ADCs from the transistor level to the architectural level are examined and a detailed design tradeoffs and strategies for radiation tolerant ADCs are described The theory and hardening techniques are supported by measurement data from a high performance RT ADC prototype chip Two important flows which are a technology evaluation flow and an RT IC design flow are also covered in order to give a complete overview on how to achieve an effective RT circuits design **Dissertation Abstracts International** ,2003 **Computer-Aided Design of Analog Integrated Circuits and Systems** Rob A. Rutenbar,Georges G. E. Gielen,2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today s new analog design automation tools Areas covered are Analog synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck *VLSI Design Techniques for Analog and Digital Circuits* Randall L. Geiger,Phillip E. Allen,Noel R. Strader,1990 *Proceedings* ,1990 **American Book Publishing Record** ,2004 *Nanoelectronic Mixed-Signal System Design* Saraju Mohanty,2015-02-20 Covering both the classical and emerging nanoelectronic technologies being used in mixed signal design this book addresses digital analog and memory components Winner of the Association of American Publishers 2016 PROSE Award in the Textbook Physical Sciences Mathematics category Nanoelectronic Mixed Signal System Design offers professionals and students a unified perspective on the science engineering and technology behind nanoelectronics system

design Written by the director of the NanoSystem Design Laboratory at the University of North Texas this comprehensive guide provides a large scale picture of the design and manufacturing aspects of nanoelectronic based systems It features dual coverage of mixed signal circuit and system design rather than just digital or analog only Key topics such as process variations power dissipation and security aspects of electronic system design are discussed Top down analysis of all stages from design to manufacturing Coverage of current and developing nanoelectronic technologies not just nano CMOS Describes the basics of nanoelectronic technology and the structure of popular electronic systems Reveals the techniques required for design excellence and manufacturability

Proceedings of the IEEE 1999 Custom Integrated Circuits Conference ,1999 Proceedings, IEEE International Conference on Computer Design, VLSI in Computers ,1985

Proceedings of the ... Midwest Symposium on Circuits and Systems ,2002 **The British National Bibliography** Arthur James Wells,2004 **Second International Conference on the Impact of High Speed and VLSI Technology on Communication Systems, 30 November-1 December 1983** Institution of Electrical Engineers. Electronics Division,1983 *Circuits for Wireless Communications* Banlue Srisuchinwong,Wanlop Surakamponporn,Sawasd Tantaratana,2001 Wireless communications is one of the fastest growing areas of communications technology This book presents current and classic papers on circuits for wireless communications Selected by three experts in the field the text should be useful for students engineers just beginning to work in cellular communications and experienced engineers who want a convenient and useful reference source *Proceedings of Euro ASIC '90* Institut national polytechnique (Grenoble). Club conception de circuits à la demande,1990 *The 5th IEE International Conference on ADDA 2005* ,2005 ADDA is a forum where experts in the field meet and exchange information on the developments in this field Due to recent growth in advanced Analogue to Digital and Digital to Analogue the topics covered were conversion techniques applications calibration testing standardisation and performance There is continuous improvement of higher speed and longer scale length devices together with new processing techniques to put more and more performance on the chip Together with this new techniques are being developed into new applications The driving force is the relentless move to replace analogue circuitry with digital

Science Abstracts ,1995

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