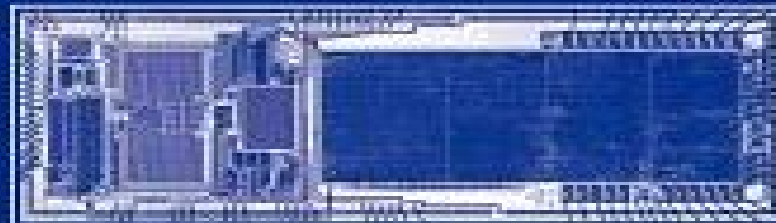


DESIGN OF WIRELESS AUTONOMOUS DATALOGGER IC'S

Wim Claes, Willy Sansen and Robert Puers



Design Of Wireless Autonomous Datalogger Ics

Karl Zum Winkel



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Prognostics and Health Management of Electronics Michael G. Pecht, 2008-09-11 The first book on Prognostics and Health Management of Electronics Recently the field of prognostics for electronic products has received increased attention due to the potential to provide early warning of system failures forecast maintenance as needed and reduce life cycle costs In response to the subject s growing interest among industry government and academic professionals this book provides a road map to the current challenges and opportunities for research and development in Prognostics and Health Management PHM The book begins with a review of PHM and the techniques being developed to enable a prognostics approach for electronic products and systems building on this foundation the book then presents the state of the art in sensor systems for in situ health and usage monitoring Next it discusses the various models and algorithms that can be utilized in PHM Finally it concludes with a discussion of the opportunities in future research Readers can use the information in this book to Detect and isolate faults Reduce the occurrence of No Fault Found NFF Provide advanced warning of system failures Enable condition based predictive maintenance Obtain knowledge of load history for future design qualification and root cause analysis Increase system availability through an extension of maintenance cycles and or timely repair actions Subtract life cycle costs of equipment from reduction in inspection costs down time and inventory Prognostics and Health Management of

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Wide-Bandwidth High Dynamic Range D/A Converters Konstantinos Doris, Arthur H.M. van Roermund, Domine Leenaerts, 2006-01-12 IGH SPEED Digital to Analog D A converters are essential components in digital communication systems providing the necessary conversion of signals encoding information in bits to signals encoding information in their amplitude vs time domain characteristics In general they are parts of a larger system the interface which consists of several signal conditioning circuits Dependent on where the converter is located within the chain of circuits in the interface signal processing operations are partitioned in those realized with digital techniques and those with analog The rapid evolution of CMOS technology has established implicit and explicit trends related to the interface and in particular to the D A converter The implicit relationship comes via the growth of digital systems First it is a global trend with respect to all interface circuits that increasing operating frequencies of digital systems place a similar demand for the interface circuits The second trend takes place locally within the interface Initially the D A converter was placed at the beginning of the interface chain and all signal conditioning was implemented in the analog domain after the D A conversion The increasing flexibility and robustness of digital signal processing shifted the D A converter closer to the end point of the chain where the demands for high quality high frequency operation are very high

Sigma Delta A/D Conversion for Signal Conditioning Kathleen Philips, Arthur H.M. van Roermund, 2006-05-05 1 1 Background Moore's Law predicts a decrease by a factor of two in the feature size of CMOS technology every three years and has been valid for years It implies a doubling of the operation speed and a four times higher transistor count per unit of area every three years The combination leads to an eight times higher processing capability per unit of area This ongoing miniaturization allows the integration of complex electronic systems with millions of transistors Very Large Scale Integration and enables the integration of electronic systems An electronic system A generic picture of an integrated electronic system is shown in figure 1 1 The heart of the system is the signal processing core This core supports a wide variety of functions such as customization and programmability of multiple applications channel coding the definition of the user interface etc These functions are enabled by DSP a controller CPU and various blocks of memory In advanced ICs these blocks provide almost all signal processing and usually dominate in the overall power and area consumption of integrated systems The huge data rates involved require high speed busses for communication between these blocks A power management unit fuels the system by providing the appropriate supply voltages and currents

Low-Power Low-Voltage Sigma-Delta Modulators in Nanometer CMOS Libin Yao, Michiel Steyaert, Willy M. C. Sansen, 2006-02-06 this book is not suitable for the bookstore catalogue

Dynamic Characterisation of Analogue-to-Digital Converters Dominique Dallet, José Machado da Silva, 2006-03-08 The Analogue to digital converter ADC is the most pervasive block in electronic systems With the advent of powerful digital signal processing and digital communication techniques ADCs are fast becoming critical components for system's performance and

flexibility Knowing accurately all the parameters that characterise their dynamic behaviour is crucial on one hand to select the most adequate ADC architecture and characteristics for each end application and on the other hand to understand how they affect performance bottlenecks in the signal processing chain Dynamic Characterisation of Analogue to Digital Converters presents a state of the art overview of the methods and procedures employed for characterising ADCs dynamic performance behaviour using sinusoidal stimuli The three classical methods histogram sine wave fitting and spectral analysis are thoroughly described and new approaches are proposed to circumvent some of their limitations This is a must have compendium which can be used by both academics and test professionals to understand the fundamental mathematics underlining the algorithms of ADC testing and as an handbook to help the engineer in the most important and critical details for their implementation

Calibration Techniques in Nyquist A/D Converters Hendrik van der Ploeg, Bram Nauta, 2006-09-13 This book analyses different A/D converter architectures with an emphasis on the maximum achievable power efficiency It also provides an accessible overview of the state of the art in calibration techniques for Nyquist A/D converters The calibration techniques presented are applicable to other analog to digital systems such as those applied in integrated receivers They allow implementation without introducing a speed or power penalty

High-Speed Photodiodes in Standard CMOS Technology Sasa Radovanovic, Anne-Johan Annema, Bram Nauta, 2006-10-11 High speed Photodiodes in Standard CMOS Technology describes high speed photodiodes in standard CMOS technology which allow monolithic integration of optical receivers for short haul communication For short haul communication the cost aspect is important and therefore it is desirable that the optical receiver can be integrated in the same CMOS technology as the rest of the system If this is possible then ultimately a single chip system including optical inputs becomes feasible eliminating EMC and crosstalk problems while data rate can be extremely high The problem of photodiodes in standard CMOS technology is that they have very limited bandwidth allowing data rates up to only 50 Mbit per second High speed Photodiodes in Standard CMOS Technology first analyzes the photodiode behaviour and compares existing solutions to enhance the speed After this the book introduces a new and robust electronic equalizer technique that makes data rates of 3 Gb/s possible without changing the manufacturing technology The application of this technique can be found in short haul fibre communication optical printed circuit boards but also photodiodes for laser disks

Precision Temperature Sensors in CMOS Technology Micheal A.P. Pertijs, Johan Huijsing, 2006-12-06 The low cost and direct digital output of CMOS smart temperature sensors are important advantages compared to conventional temperature sensors This book addresses the main problem that nevertheless prevents widespread application of CMOS smart temperature sensors their relatively poor absolute accuracy Several new techniques are introduced to improve this accuracy The effectiveness of these techniques is demonstrated using three prototypes The final prototype achieves an inaccuracy of 0.1 °C over the military temperature range which is a significant improvement in the state of the art Since smart temperature sensors have been the subject of academic and industrial

research for more than two decades an overview of existing knowledge and techniques is also provided throughout the book In this introductory chapter the motivation and objectives of this work are described

This is followed by a review of the basic operating principles of CMOS smart temperature sensors and a brief overview of previous work The challenges are then described that need to be met in order to improve the accuracy of CMOS smart temperature sensors while maintaining their cost advantage Finally the structure of the rest of the book is introduced RF Power

Amplifiers for Mobile Communications Patrick Reynaert, Michiel Steyaert, 2006-11-18 This book tackles both high efficiency and high linearity power amplifier PA design in low voltage CMOS With its emphasis on theory design and implementation the book offers a guide for those actively involved in the design of fully integrated CMOS wireless transceivers Offering mathematical background as well as intuitive insight the book is essential reading for RF design engineers and researchers and is also suitable as a text book **Design of Wireless Autonomous Datalogger IC's** Wim Claes, Willy M. C.

Sansen, Robert Puers, 2005-07-13 Design of Wireless Autonomous Dataloggers IC's reveals the state of the art in the design of complex dataloggers with a special focus on low power consumption The emphasis is on autonomous dataloggers for stand alone applications with remote reprogrammability The book starts with a comprehensive introduction on the most important design aspects and trade offs for miniaturized low power telemetric dataloggers After the general introduction follows an in depth case study of an autonomous CMOS datalogger IC for the registration of in vivo loads on oral implants After tackling the design of the datalogger on the system level the design of the different building blocks is elaborated in detail with emphasis on low power A clear overview of the operation the implementation and the most important design considerations of the building blocks to achieve optimal system performance is given Design of Wireless Autonomous Dataloggers IC's discusses the design of correlated double sampling amplifiers and sample and holds binary weighted current steering DACs successive approximation ADCs and relaxation clock oscillators and can also be used as a manual for the design of these building blocks Design of Wireless Autonomous Dataloggers IC's covers the complete design flow of low power miniaturized autonomous dataloggers with a bi directional wireless link and on board data processing while providing detailed insight into the most critical design issues of the different building blocks It will allow you to design complex dataloggers faster It is essential reading for analog design engineers and researchers in the field of miniaturized dataloggers and is also suitable as a text for an advanced course on the subject **Electrical & Electronics Abstracts**, 1997 **Design of an Autonomous**

Decentralized MAC Protocol for Wireless Sensor Networks L. F. W. van Hoesel, L. Dal Pont, Paul Johannes Mattheus Havinga, 2003 Nuklearmedizin Karl Zum Winkel, 1990 **An IC Design for Wireless Batteryless Multi-sensing**

Platform for Biomedical Applications Young-Sik Seo, 2012 *Autonomous Small Scale Data-logger for Temperature Mapping* Ahmed G. Tawfik, 2015 Modern manufacturing processes require minimal human intervention and a high degree of automation to meet industry demands Due to variability in industrial process conditions custom systems are often sought for

these applications These systems must be compact economical and capable of operating under different environmental conditions This work presents the development fabrication testing and validation of a low cost small scale temperature data logger used as a monitoring system for automated applications The proposed system is battery powered and packaged in a manner able to operate in temperatures up to 100oC with exposure to chemicals such as Isopropyl Alcohol Propylene Glycol and De Ionized water for a period of 2 hours with accuracy of 0.5oC The hydration process used for contact lens manufacturing is proposed as a target application for the developed system The developed system was bench top tested and validated using a convection oven and the three chemicals Propylene Glycol Isopropyl Alcohol and De ionized Water In addition the system was tested in situ in the hydration lines of a contact lens manufacturing process The development process illustrated in this work including the system design fabrication and testing can be used as a base to develop the best fit monitoring system for multiple other applications

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