

10-Year Retrospective Most Influential Paper Award

The 18th Asia and South Pacific Design Automation Conference (ASP-DAC 2013)

"Optimizing Routability in Large-Scale Mixed-Size Placement"

Jason Cong (Univ. of California, Los Angeles, U.S.A.),
Guojie Luo (Peking Univ., China),
Kalliopi Tsota (Univ. of California, Los Angeles, U.S.A.),
Bingjun Xiao (Univ. of California, Los Angeles, U.S.A.)

Design Automation Conference 1998 Asia And South Pacific

Association for Computing Machinery

A red circular graphic with a gradient, appearing as a stylized arrow or a partial circle, located to the right of the Association for Computing Machinery text.

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ASP-DAC 98 Association for Computing Machinery,1998-01-01 *Proceedings of the ASP-DAC '98* ,2002

Proceedings of the ASP-DAC ... Asia and South Pacific Design Automation Conference ,2002 **Approximate**

Computing Weiqiang Liu,Fabrizio Lombardi,2022-08-22 This book explores the technological developments at various levels of abstraction of the new paradigm of approximate computing The authors describe in a single source the state of the art covering the entire spectrum of research activities in approximate computing bridging device circuit architecture and system levels Content includes tutorials reviews and surveys of current theoretical experimental results design methodologies and applications developed in approximate computing for a wide scope of readership and specialists Serves as a single source reference to state of the art of approximate computing Covers broad range of topics from circuits to applications Includes contributions by leading researchers from academia and industry **Electronic Design Automation for IC**

Implementation, Circuit Design, and Process Technology Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-02-03 The second of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic RTL to GDSII a file format used to transfer data of semiconductor physical layout design flow analog mixed signal design physical verification and technology computer aided design TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability DFM at the nanoscale power supply network design and analysis design modeling and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on 3D circuit integration and clock design Offering improved depth and modernity Electronic Design Automation for IC Implementation Circuit Design and Process Technology provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals **Circuit Design for Reliability** Ricardo Reis,Yu Cao,Gilson

Wirth,2014-11-08 This book presents physical understanding modeling and simulation on chip characterization layout solutions and design techniques that are effective to enhance the reliability of various circuit units The authors provide readers with techniques for state of the art and future technologies ranging from technology modeling fault detection and analysis circuit hardening and reliability management **Proceedings** ,2003 **Digital Watermarking** Fabien

Petitcolas,Hyoung Joong Kim,2003-08-03 The 1st International Workshop on Digital Watermarking IWDW the conference covering all aspects of digital watermarking was held at the Hotel Riviera situated along the beautiful Han River in Seoul

Korea from November 21 to 22 2002 These proceedings contain 21 papers that were accepted for presentation at the conference These papers were selected from 67 submissions including 3 invited papers They went through a thorough review process by the Program Committee and were selected on the basis of excellence and novelty The following is a brief description of the history of this conference and viewing process In August 2001 some members of the Special Interest Group on Multimedia Protection SIGMP of the Korea Institute of Information Security and Cryptology KIISC agreed to create the IWDW In November 2001 we set up a Program Committee and solicited papers while asking Springer Verlag to publish the proceedings of the workshop in their Lecture Notes in Computer Science series In July 2002 we received 64 submissions from 14 countries using Microsoft's conference management site <http://cmt.research.microsoft.com/iwdw2002> Each submission was assigned a number automatically by the conference management tool and the paper was sent to the Program Committee members for their review We also encouraged different sets of experts to join for fair reviews

The Fourth International Conference/Exhibition on High-Performance Computing in the Asia-Pacific Region, Beijing, China, May 14-17, 2000, 2000 **Advanced Field-Solver Techniques for RC Extraction of Integrated Circuits** Wenjian Yu, Xiren Wang, 2014-04-21

Resistance and capacitance RC extraction is an essential step in modeling the interconnection wires and substrate coupling effect in nanometer technology integrated circuits IC The field solver techniques for RC extraction guarantee the accuracy of modeling and are becoming increasingly important in meeting the demand for accurate modeling and simulation of VLSI designs Advanced Field Solver Techniques for RC Extraction of Integrated Circuits presents a systematic introduction to and treatment of the key field solver methods for RC extraction of VLSI interconnects and substrate coupling in mixed signal ICs Various field solver techniques are explained in detail with real world examples to illustrate the advantages and disadvantages of each algorithm This book will benefit graduate students and researchers in the field of electrical and computer engineering as well as engineers working in the IC design and design automation industries Dr Wenjian Yu is an Associate Professor at the Department of Computer Science and Technology at Tsinghua University in China Dr Xiren Wang is a R D Engineer at Cadence Design Systems in the USA

2021 26th Asia and South Pacific Design Automation Conference (ASP-DAC). Shinobu Nagayama, Kohei Miyase, 2021 Essential Issues in SOC Design Youn-Long Steve Lin, 2007-05-31

This book originated from a workshop held at the DATE 2005 conference namely Designing Complex SOC's State of the art in issues related to System on Chip SoC design by leading experts in the fields covers IP development verification integration chip implementation testing and software SOC design is fast becoming the key area of focus that engineers and researchers from the Electronic Design Automation field are focusing on in their quest to further develop Integrated Circuit technology The more systems and even networks that we can integrate on one piece of silicon the faster cheaper more powerful and efficient the technology will become Essential Issues in SOC Design contains valuable academic and industrial examples for those involved with the design of complex SOC's all contributors are selected

from a region of the world that is generally known to lead the SOC Revolution namely Asia *Index of Conference Proceedings* British Library. Document Supply Centre, 2003 **VLSI Design 2001 : Fourteenth International Conference on VLSI Design** VLSI Society of India, 2001 The International Conference on VLSI Design was started in 1985 as a workshop and from this start has grown into an international conference on VLSI design The proceedings are dedicated to all aspects of integrated circuit design technology and related computer aided design CAD **Fourth International Symposium on Quality Electronic Design** , 2003 ISQED looks into the research development and application of design techniques and methods design processes and EDA design methodologies and tools that address issues that impact the quality of the realization of designs into physical integrated circuits It emphasizes a holistic approach toward design quality and to highlight and accelerate cooperation among the IC Design EDA Semiconductor Process Technology and Manufacturing communities *Proceedings of the 24th Asia and South Pacific Design Automation Conference* Toshiyuki Shibuya, 2019 **10th International Symposium on Asynchronous Circuits and Systems** , 2004 IEEE Computer Society Order Number P2133 T p verso **Configurable Computing** , 1998 This collection of papers presented at the IS T SPIE Electronic Imaging Symposium includes articles on a variety of relevant issues and topics *Digest of Technical Papers* , 2001 **Scalable Partitioning-driven Algorithms for Solving Complex and Emerging Problems in VLSI Physical Design Automation** Navaratnasothie Selvakkumaran, 2005

Whispering the Secrets of Language: An Mental Quest through **Design Automation Conference 1998 Asia And South Pacific**

In a digitally-driven earth where screens reign supreme and instant conversation drowns out the subtleties of language, the profound secrets and emotional nuances concealed within words frequently move unheard. Yet, located within the pages of **Design Automation Conference 1998 Asia And South Pacific** a captivating literary prize pulsing with natural thoughts, lies an exceptional quest waiting to be undertaken. Written by an experienced wordsmith, that marvelous opus invites visitors on an introspective journey, gently unraveling the veiled truths and profound affect resonating within the very cloth of each word. Within the mental depths of this moving evaluation, we shall embark upon a sincere exploration of the book is core subjects, dissect its fascinating writing fashion, and fail to the effective resonance it evokes heavy within the recesses of readers hearts.

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