

WORKSHOPS IN COMPUTING

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Geraint Jones and
Mary Sheeran (Eds.)

Designing Correct Circuits, Oxford 1990



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Designing Correct Circuits

Didier Musso



Designing Correct Circuits:

Designing Correct Circuits Geraint Jones, Mary Sheeran, 2013-12-14 These proceedings contain the papers presented at a workshop on Designing Correct Circuits jointly organised by the Universities of Oxford and Glasgow and held in Oxford on 26-28 September 1990 There is a growing interest in the application to hardware design of the techniques of software engineering As the complexity of hardware systems grows and as the cost both in money and time of making design errors becomes more apparent so there is an eagerness to build on the success of mathematical techniques in program development The harsher constraints on hardware designers mean both that there is a greater need for good abstractions and rigorous assurances of the trustworthiness of designs and also that there is greater reason to expect that these benefits can be realised The papers presented at this workshop consider the application of mathematics to hardware design at several different levels of abstraction At the lowest level of this spectrum Zhou and Hoare show how to describe and reason about synchronous switching circuits using UNiLY a formalism that was developed for reasoning about parallel programs Aagaard and Leeser use standard mathematical techniques to prove correct their implementation of an algorithm for Boolean simplification The circuits generated by their formal synthesis system are thus correct by construction Thuau and Pilaud show how the declarative language LUSTRE which was designed for programming real time systems can be used to specify synchronous circuits

Automated Technology for Verification and Analysis Zhiming Liu, Anders P. Ravn, 2009-10-13 This book constitutes the proceedings of the 7th International Symposium on Automated Technology for Verification and Analysis ATVA 2009 held in Macao China in October 2009 The 23 regular papers and 3 tool papers presented together with 3 invited talks were carefully reviewed and selected from 74 research papers and 10 tool papers submissions The papers are organized in topical sections on state space reduction tools probabilistic systems model temporal logic abstraction and refinement and fault tolerant systems

Special Issue on Designing Correct Circuits, 1993 Designing Correct Circuits Jørgen Staunstrup, 1993

Designing Correct Circuits Mary Sheeran, 1996 The main theme of the 3rd Workshop on Designing Correct Circuits DCC96 held in BA stad Sweden 2-4 September 1996 was the discussion of the very latest advances in hardware verification and correct circuit design techniques Among the main topics covered are improving hardware designs by simplifying their proof an integrated approach to verifying large circuits deriving handshake modules for a multi target hardware compiler and serialising heterogeneous and non factorisable processor arrays The full papers from Designing Correct Circuits are available on line at <http://www.springer.co.uk/eWiC/Workshops/DCC96.html> This booklet provides the passwords needed to access the web site Purchasing the booklet provides the user with unlimited access to the site for four years subject of course to entry of the correct passwords each time

A Circuit Grammar for Operational Amplifier Design Andrew Lewis Ressler, 1984 Electrical circuit designers seldom create really new topologies or use old ones in a novel way Most designs are known combinations of common configurations tailored for the particular problem at hand In

this thesis I show that much of the behavior of a designer engaged in such ordinary design can be modeled by a clearly defined computational mechanism executing a set of stylized rules Each of my rules embodies a particular piece of the designer's knowledge A circuit is represented as a hierarchy of abstract objects each of which is composed of other objects The leaves of this tree represent the physical devices from which physical circuits are fabricated By analogy with context free languages a class of circuits is generated by a phrase structure grammar of which each rule describes how one type of abstract object can be expanded into a combination of more concrete parts Circuits are designed by first postulating an abstract object which meets the particular design requirements This object is then expanded into a concrete circuit by successive refinement using rules of my grammar There are in general many rules which can be used to expand a given abstract component Analysis must be done at each level of the expansion to constrain the search to a reasonable set

Designing Pneumatic Control Circuits Bruce E. McCord,1983 Henley's Twentieth Century Formulas, Recipes and Processes; Containing Ten Thousand Selected Household and Workshop, Formulas, Recipes, Processes and Money-saving Ideas Gardner Dexter Hiscox,1924 *Designing Correct Circuits* Jørgen Staunstrup,Robin Sharp,1992 The papers compiled in this volume cover a wide range of topics within the general area of provably correct design including proofs of fundamental results post hoc formal verification of designs and design techniques where correctness is ensured by construction The papers are unique in that they demonstrate the use of design techniques for designing real chips The book should thus not be missed by students and researchers interested in the deployment of formally based techniques on real problems within a great diversity of application areas Application areas treated within the volume range from packet routers arbiters multiprocessors serial parallel converters and CPUs to arithmetic units and counters A significant group of the papers is concerned with the special requirements of asynchronous and delay insensitive design International Conference on Computer Aided Design and Manufacture of Electronic Components, Circuits, and Systems, 3-6 July 1979, University of Sussex ,1979 **The Design of Gravity-circulation Water Heating Systems** Frederick Ernest Giesecke,1926
Automotive Industries ,1915 *Telegraph and Telephone Age* ,1913 *Designing Correct Circuits* Jørgen Staunstrup,1992 *Registration of Designs* United States. Congress. House. Committee on Patents,1916 **Electricity in the Service of Man** R. Mullineux Walmsley,1921 **Ship Model Making** E. Armitage McCann,1926 **Mechanical Engineering** American Society of Mechanical Engineers,1947 *The British National Bibliography* Arthur James Wells,1998 **Electrical Installation Record** ,1919

Whispering the Strategies of Language: An Psychological Quest through **Designing Correct Circuits**

In a digitally-driven earth where monitors reign great and quick conversation drowns out the subtleties of language, the profound secrets and psychological nuances hidden within phrases usually move unheard. Yet, set within the pages of **Designing Correct Circuits** a captivating literary value blinking with fresh thoughts, lies an exceptional quest waiting to be undertaken. Published by a talented wordsmith, this charming opus attracts readers on an introspective journey, softly unraveling the veiled truths and profound impact resonating within the very fabric of every word. Within the mental depths of the touching evaluation, we can embark upon a sincere exploration of the book is primary subjects, dissect its interesting writing fashion, and succumb to the effective resonance it evokes serious within the recesses of readers hearts.

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