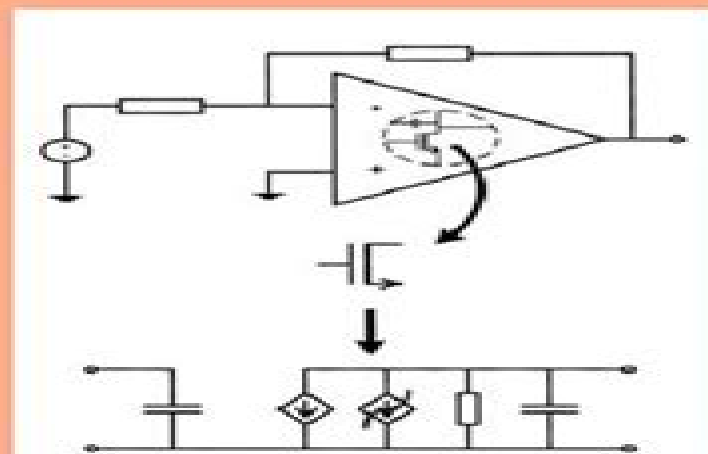


DESIGN CRITERIA FOR LOW DISTORTION IN FEEDBACK OPAMP CIRCUITS

Bjørnar Hernes and Trond Sæther

Foreword by Willy Sansen



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Design Criteria For Low Distortion In Feedback Opamp Circuits

Yicheng Fang



Design Criteria For Low Distortion In Feedback Opamp Circuits:

Design Criteria for Low Distortion in Feedback Opamp Circuits Bjørnar Hernes, Trond Sæther, 2003-01-31 Broadband opamps for multi channel communication systems make strong demands on linearity performance This book written for Analog CMOS designers presents a thorough analysis of the nonlinear behaviour of circuits to obtain opamps with low distortion CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters Rudy J. van de Plassche, 2013-04-17 CMOS Integrated Analog to Digital and Digital to Analog Converters describes in depth converter specifications like Effective Number of Bits ENOB Spurious Free Dynamic Range SFDR Integral Non Linearity INL Differential Non Linearity DNL and sampling clock jitter requirements Relations between these specifications and practical issues like matching of components and offset parameters of differential pairs are derived CMOS Integrated Analog to Digital and Digital to Analog Converters describes the requirements of input and signal reconstruction filtering in case a converter is applied into a signal processing system CMOS Integrated Analog to Digital and Digital to Analog Converters describes design details of high speed A/D and D/A converters high resolution A/D and D/A converters sample and hold amplifiers voltage and current references noise shaping converters and sigma delta converters technology parameters and matching performance comparators and limitations of comparators and finally testing of converters Analog Filters in Nanometer CMOS Heimo Uhrmann, Robert Kolm, Horst Zimmermann, 2013-08-15 Starting from the basics of analog filters and the poor transistor characteristics in nanometer CMOS 10 high performance analog filters developed by the authors in 120 nm and 65 nm CMOS are described extensively Among them are gm/C filters current mode filters and active filters for system on chip realization for Bluetooth WCDMA UWB DVB-H and LTE applications For the active filters several operational amplifier designs are described The book furthermore contains a review of the newest state of research on low voltage low power analog filters To cover the topic of the book comprehensively linearization issues and measurement methods for the characterization of advanced analog filters are introduced in addition Numerous elaborate illustrations promote an easy comprehension This book will be of value to engineers and researchers in industry as well as scientists and Ph.D. students at universities The book is also recommendable to graduate students specializing on nanoelectronics microelectronics or circuit engineering Static and Dynamic Performance Limitations for High Speed D/A Converters Anne van den Bosch, Michiel Steyaert, Willy M.C. Sansen, 2013-06-29 Static and Dynamic Performance Limitations for High Speed D/A Converters discusses the design and implementation of high speed current steering CMOS digital to analog converters Starting from the definition of the basic specifications for a D/A converter the elements determining the static and dynamic performance are identified Different guidelines based on scientific derivations are suggested to optimize this performance Furthermore a new closed formula has been derived to account for the influence of the transistor mismatch on the achievable resolution of the current steering D/A converter To allow a thorough understanding of the dynamic behavior a new factor has been introduced Moreover the

frequency dependency of the output impedance introduces harmonic distortion components which can limit the maximum attainable spurious free dynamic range Finally the last part of the book gives an overview on different existing transistor mismatch models and the link with the static performance of the D A converter

Systematic Modeling and Analysis of Telecom Frontends and their Building Blocks Piet Vanassche, Georges Gielen, Willy M. C. Sansen, 2005-09-01 To meet the demands of today's highly competitive market analog electronics designers must develop their IC designs in a minimum of time The difference between first and second time right seriously affects a company's share of the market Analog designers are therefore in need for structured design methods together with the theory and tools to support them especially when pushing the performance limits in high performance designs Systematic Modeling and Analysis of Telecom Frontends and Their Building Blocks aims to help designers in speeding up telecommunication frontend design by offering an in depth understanding of the frontend's behavior together with methods and algorithms that support designers in bringing this understanding to practice The book treats topics such as time varying phase locked loop stability noise in mixing circuits oscillator injection locking oscillator phase noise behavior harmonic oscillator dynamics and many more In doing so it always starts from a theoretical foundation that is both rigorous and general Phase locked loop and mixer analysis for example are grounded upon a general framework for time varying small signal analysis Likewise analysis of harmonic oscillator transient behavior and oscillator phase noise analysis are treated as particular applications of a general framework for oscillator perturbation analysis In order to make the book as easy to read as possible all theory is always accompanied by numerous examples and easy to catch intuitive explanations As such the book is suited for both computer aided design engineers looking for general theories and methods either as background material or for practical implementation in tools as well as for practicing circuit designers looking for help and insight in dealing with a particular application or a particular high performance design problem

Mixed-Signal Layout Generation Concepts Chieh Lin, Arthur H.M. van Roermund, Domine Leenaerts, 2005-12-15 This title covers important physical design issues that exist in contemporary analogue and mixed signal design flows The authors bring together many principles and techniques required to successfully develop and implement layout generation tools to accommodate many mixed signal layout generation needs

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IEEE Circuits & Devices ,2004 **Analog Design Essentials** Willy M. C. Sansen,2006-05-22 This unique book contains all topics of importance to the analog designer which are essential to obtain sufficient insights to do a thorough job The book starts with elementary stages in building up operational amplifiers The synthesis of opamps is covered in great detail Many examples are included operating at low supply voltages Chapters on noise distortion filters ADC DACs and oscillators follow These are all based on the extensive amount of teaching that the author has carried out world wide

The British National Bibliography Arthur James Wells,2003 **Book Review Index** ,2004 Every 3rd issue is a quarterly cumulation *American Book Publishing Record* ,2003 **Op Amps and Linear Integrated Circuits for Technicians** Frank R. Dungan,1992 **Optimal Design of Modulators for Oversampled Converters** Feng Wang,1997 **Audio IC Op-amp Applications** Walter G. Jung,1986 **Semiconductor Master Selection Guide** ,1988 Digital Integrated Circuits and Operational-amplifier and Optoelectronic Circuit Design Bryan Norris,1976 Digital integrated circuits Operational amplifiers Optoelectronics *Proceedings of the ... Midwest Symposium on Circuits and Systems* ,2001 Wireless World ,1982 **Applied Science & Technology Index** ,1983

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