DESIGN AUTOMATION FOR TIMING-DRIVEN LAYOUT SYNTHESIS
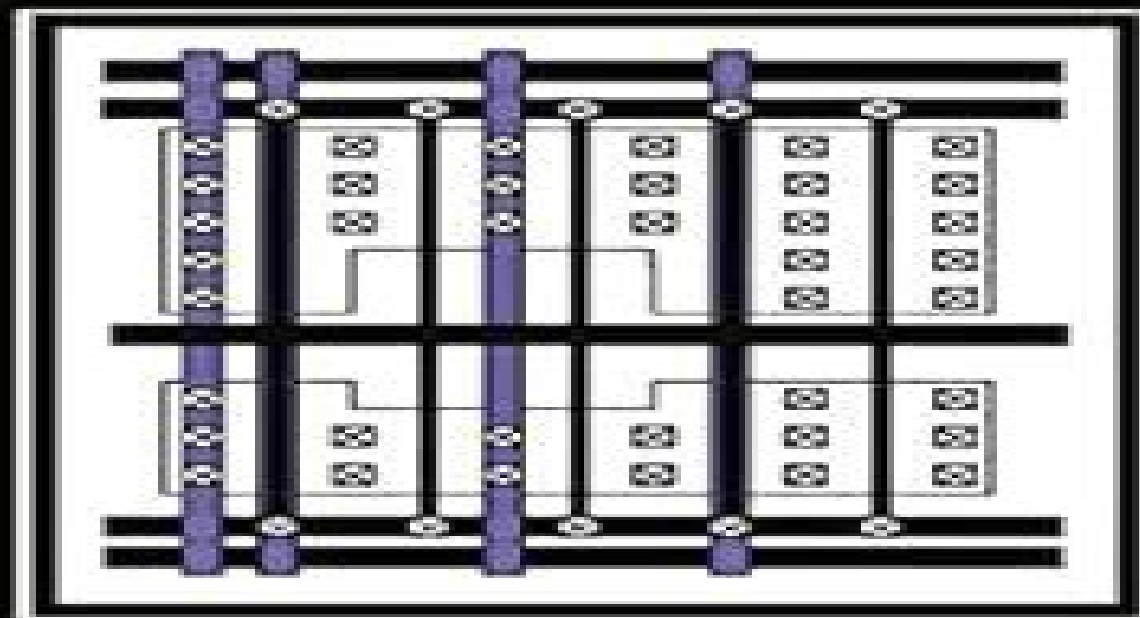
Sachin S. Sapatnekar • S. M. (Steve) Kang

Design Automation For Timing Driven Layout Synthesis

Qing Zhu

Design Automation For Timing Driven Layout Synthesis:

Design Automation for Timing-Driven Layout Synthesis S. Sapatnekar,Sung-Mo (Steve) Kang,2012-10-04 Moore s law Noy77 which predicted that the number of devices in tegrated on a chip would be doubled every two years was accurate for a number of years Only recently has the level of integration be gun to slow down somewhat due to the physical limits of integration technology Advances in silicon technology have allowed Ie design ers to integrate more than a few million transistors on a chip even a whole system of moderate complexity can now be implemented on a single chip To keep pace with the increasing complexity in very large scale integrated VLSI circuits the productivity of chip designers would have to increase at the same rate as the level of integration Without such an increase in productivity the design of complex systems might not be achievable within a reasonable time frame The rapidly increasing complexity of VLSI circuits has made de 1 2 INTRODUCTION sign automation an absolute necessity since the required increase in productivity can only be accomplished with the use of sophisticated design tools Such tools also enable designers to perform trade off analyses of different logic implementations and to make well informed design decisions   Electronic Design Automation for IC Implementation, Circuit Design, and Process Technology Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-02-03 The second of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic RTL to GDSII a file format used to transfer data of semiconductor physical layout design flow analog mixed signal design physical verification and technology computer aided design TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability DFM at the nanoscale power supply network design and analysis design modeling and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on 3D circuit integration and clock design Offering improved depth and modernity Electronic Design Automation for IC Implementation Circuit Design and Process Technology provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals   **An Analog VLSI System for Stereoscopic Vision** Misha Mahowald,2012-12-06 An Analog VLSI System for Stereoscopic Vision investigates the interaction of the physical medium and the computation in both biological and analog VLSI systems by synthesizing a functional neuromorphic system in silicon In both the synthesis and analysis of the system a point of view from within the system is adopted rather than that of an omniscient designer drawing a blueprint This perspective projects the design and the designer into a living landscape The motivation for a machine centered perspective is

explained in the first chapter The second chapter describes the evolution of the silicon retina The retina accurately encodes visual information over orders of magnitude of ambient illumination using mismatched components that are calibrated as part of the encoding process The visual abstraction created by the retina is suitable for transmission through a limited bandwidth channel The third chapter introduces a general method for interchip communication the address event representation which is used for transmission of retinal data The address event representation takes advantage of the speed of CMOS relative to biological neurons to preserve the information of biological action potentials using digital circuitry in place of axons The fourth chapter describes a collective circuit that computes stereodisparity In this circuit the processing that corrects for imperfections in the hardware compensates for inherent ambiguity in the environment The fifth chapter demonstrates a primitive working stereovision system An Analog VLSI System for Stereoscopic Vision contributes to both computer engineering and neuroscience at a concrete level Through the construction of a working analog of biological vision subsystems new circuits for building brain style analog computers have been developed Specific neuropysiological and psychophysical results in terms of underlying electronic mechanisms are explained These examples demonstrate the utility of using biological principles for building brain style computers and the significance of building brain style computers for understanding the nervous system   *EDA for IC Implementation, Circuit Design, and Process Technology* Luciano Lavagno,Louis Scheffer,Grant Martin,2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The second volume EDA for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic to GDSII a file format used to transfer data of semiconductor physical layout analog mixed signal design physical verification and technology CAD TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability at the nanoscale power supply network design and analysis design modeling and much more Save on the complete set   **Field Programmable Logic and Application** Jürgen Becker,Marco Platzner,Serge Vernalde,2004-08-19 This book constitutes the refereed proceedings of the 14th International Conference on Field Programmable Logic FPL 2003 held in Leuven Belgium in August September 2004 The 78 revised full papers 45 revised short papers and 29 poster abstracts presented together with 3 keynote contributions and 3 tutorial summaries were carefully reviewed and selected from 285 papers submitted The papers are organized in topical sections on organic and biologic computing security and cryptography platform based design algorithms and architectures acceleration application architecture physical design arithmetic multitasking circuit technology network processing testing applications signal processing computational models and compiler dynamic reconfiguration networks and optimisation algorithms system on chip high speed design image processing network on chip power aware design IP based design co processing architectures system level design physical interconnect computational models cryptography and compression network applications and

architecture and debugging and test **Embedded Systems Handbook 2-Volume Set** Richard Zurawski,2018-10-08 During the past few years there has been an dramatic upsurge in research and development implementations of new technologies and deployments of actual solutions and technologies in the diverse application areas of embedded systems These areas include automotive electronics industrial automated systems and building automation and control Comprising 48 chapters and the contributions of 74 leading experts from industry and academia the Embedded Systems Handbook Second Edition presents a comprehensive view of embedded systems their design verification networking and applications The contributors directly involved in the creation and evolution of the ideas and technologies presented offer tutorials research surveys and technology overviews exploring new developments deployments and trends To accommodate the tremendous growth in the field the handbook is now divided into two volumes New in This Edition Processors for embedded systems Processor centric architecture description languages Networked embedded systems in the automotive and industrial automation fields Wireless embedded systems Embedded Systems Design and Verification Volume I of the handbook is divided into three sections It begins with a brief introduction to embedded systems design and verification The book then provides a comprehensive overview of embedded processors and various aspects of system on chip and FPGA as well as solutions to design challenges The final section explores power aware embedded computing design issues specific to secure embedded systems and web services for embedded devices Networked Embedded Systems Volume II focuses on selected application areas of networked embedded systems It covers automotive field industrial automation building automation and wireless sensor networks This volume highlights implementations in fast evolving areas which have not received proper coverage in other publications Reflecting the unique functional requirements of different application areas the contributors discuss inter node communication aspects in the context of specific applications of networked embedded systems __Chip and Package Co-synthesis of Clock Networks__ Qing Zhu,1995 *Circuit Synthesis with VHDL* Roland Airiau,Jean-Michel Bergé,Vincent Olive,1994-02-28 Modeling styles discussed are independent of specific market tools and focus on constructs recognized as synthesizable by synthesis tools The authors present two approaches to synthesis one starting with VHDL features and deriving hardware counterparts and the second starting from a given hardware component and deriving several description styles They also discuss how to introduce the synthesis design cycle into existing design methodologies The book concludes with a case study Annotation copyright by Book News Inc Portland OR __Motion Analysis and Image Sequence Processing__ M. Ibrahim Sezan,Reginald L. Lagendijk,1993-03-31 Brings together the fundamentals of various aspects of motion estimation and image sequence processing as well as the most recent developments and applications such as data compression noise suppression deblurring and motion compensated spatiotemporal interpolation and sampling The volume can be used as a textbook in an advanced level course on image sequence or video processing or as a reference for engineers researchers graduate students and faculty undertaking or planning research in image sequence processing Annotation

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