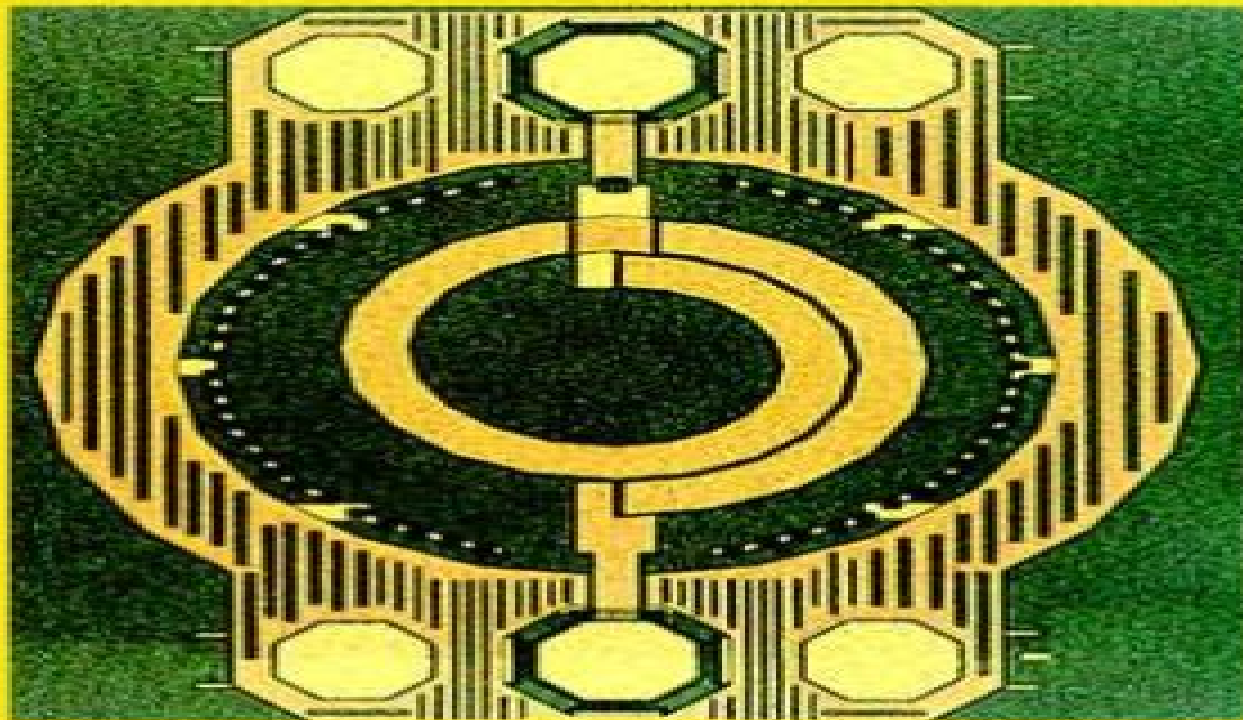


Design and Test of Integrated Inductors for RF Applications

Jaime Aguilera and Roc Berenguer



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Design and Test of Integrated Inductors for RF Applications Jaime Aguilera, Roc Berenguer, 2007-05-08 Design and Test of Integrated Inductors for RF Applications is intended for engineers who are starting out in the design of integrated inductors this due to the fact that it describes the whole design flow basic selection of the geometry optimisation of the quality by redesigning the geometry measurement and de embedding and characterisation Secondly it will help the designer with much experience in this field this due to the fact that based on empirical data some design rules that have been widely used by the design community have been proved to be really conservative and breaking them leads up to higher quality designs

High-Frequency Magnetic Components Marian K. Kazimierczuk, 2011-08-24 If you are looking for a complete study of the fundamental concepts in magnetic theory read this book No other textbook covers magnetic components of inductors and transformers for high frequency applications in detail This unique text examines design techniques of the major types of inductors and transformers used for a wide variety of high frequency applications including switching mode power supplies SMPS and resonant circuits It describes skin effect and proximity effect in detail to provide you with a sound understanding of high frequency phenomena As well as this you will discover thorough coverage on integrated inductors and the self capacitance of inductors and transformers with expressions for self capacitances in magnetic components criteria for selecting the core material as well as core shape and size and an evaluation of soft ferromagnetic materials used for magnetic cores winding resistance at high frequencies expressions for winding and core power losses when non sinusoidal inductor or transformer current waveforms contain harmonics Case studies practical design examples and procedures using the area product method and the geometry coefficient method are expertly combined with concept orientated explanations and student friendly analysis Supplied at the end of each chapter are summaries of the key concepts review questions and problems the answers to which are available in a separate solutions manual Such features make this a fantastic textbook for graduates senior level undergraduates and professors in the area of power electronics in addition to electrical and computer engineering This is also an inimitable reference guide for design engineers of power electronics circuits high frequency transformers and inductors in areas such as SMPS and RF power amplifiers and circuits *On-Wafer Microwave*

Measurements and De-embedding Errikos Lourandakis, 2016-07-31 This new authoritative resource presents the basics of network analyzer measurement equipment and troubleshooting errors involved in the on wafer microwave measurement process This book bridges the gap between theoretical and practical information using real world practices that address all aspects of on wafer passive device characterization in the microwave frequency range up to 60GHz Readers find data and measurements from silicon integrated passive devices fabricated and tested in advance CMOS technologies Basic circuit equations terms and fundamentals of time and frequency domain analysis are covered This book also explores the basics of vector network analyzers VNA two port S parameter measurement routines signal flow graphs network theory error models

and VNA calibrations with the use of calibration standards *Emerging Trends in Technological Innovation* Luis M. Camarinha-Matos, Pedro Pereira, Luis Ribeiro, 2010-02-26 Identifying Emerging Trends in Technological Innovation Doctoral programs in science and engineering are important sources of innovative ideas and techniques that might lead to new products and technological innovation Certainly most PhD students are not experienced researchers and are in the process of learning how to do research Nevertheless a number of empiric studies also show that a high number of technological innovation ideas are produced in the early careers of researchers The combination of the eagerness to try new approaches and directions of young doctoral students with the experience and broad knowledge of their supervisors is likely to result in an important pool of innovation potential The DoCEIS doctoral conference on Computing Electrical and Industrial Engineering aims at creating a space for sharing and discussing ideas and results from doctoral research in these inter related areas of engineering Innovative ideas and hypotheses can be better enhanced when presented and discussed in an encouraging and open environment DoCEIS aims to provide such an environment releasing PhD students from the pressure of presenting their propositions in more formal contexts **Radio-Frequency Microelectronic Circuits for Telecommunication**

Applications Yannis E. Papananos, 2013-03-09 Radio Frequency Microelectronic Circuits for Telecommunication Applications covers the design issues of radio frequency microelectronic circuits for telecommunication applications with emphasis on devices and circuit level design It uses a large number of real examples from industrial design as a vehicle both to teach the principles and to ensure relevance starting from device level modeling to basic RF microelectronic circuit cell design Modeling for high frequency operation of both active and passive integrated devices is covered starting from the bipolar transistor to the MOS transistor to the modeling of integrated spiral inductors resistors capacitors varactors and package parasitics structures A chapter is also devoted to the presentation of the basic definitions and terminology used in RF IC design The book continues with the presentation of the principal building blocks of an integrated RF front end namely the LNA the mixer the VCO and integrated filters Design paradigms are provided classified on the technology used in each case pure bipolar CMOS BiCMOS or SiGe Radio Frequency Microelectronic Circuits for Telecommunication Applications is essential reading for all researchers practising engineers and designers working in RF electronics It is also a reference for use in advanced undergraduate or graduate courses in the same field VLSI Circuits and Systems ,2005 **IEEE**

Circuits & Devices ,2006 **Japanese Journal of Applied Physics** ,2006 **Three-Dimensional Integrated Circuit Design** Yuan Xie, Jingsheng Jason Cong, Sachin Sapatnekar, 2009-12-02 We live in a time of great change In the electronics world the last several decades have seen unprecedented growth and advancement described by Moore's law This observation stated that transistor density in integrated circuits doubles every 1.5 to 2 years This came with the simultaneous improvement of individual device performance as well as the reduction of device power such that the total power of the resulting ICs remained under control No trend remains constant forever and this is unfortunately the case with Moore's law The trouble

began a number of years ago when CMOS devices were no longer able to proceed along the classical scaling trends. Key device parameters such as gate oxide thickness were simply no longer able to scale. As a result, device state currents began to creep up at an alarming rate. These continuing problems with classical scaling have led to a leveling off of IC clock speeds to the range of several GHz. Of course, chips can be clocked higher, but the thermal issues become unmanageable. This has led to the recent trend toward microprocessors with multiple cores, each running at a few GHz at the most. The goal is to continue improving performance via parallelism by adding more and more cores instead of increasing speed. The challenge here is to ensure that general purpose codes can be efficiently parallelized. There is another potential solution to the problem of how to improve CMOS technology performance: three dimensional integrated circuits (3D ICs).

International Symposium on Quality Electronic Design, 2002. Annotation: Fifty one papers and 21 posters from the March 2002 symposium report current research in deep submicron integrated circuit design and development. The sessions address interconnect extraction and modeling; design for process variations; metrics; power and noise management; verification; signal integrity; and low power design techniques. Some of the topics are: transition aware global signaling; TAGS: the interoperability of EDA tools for sequential logic synthesis; statistical methods for the determination of process corners; power supply noise suppression via clock skew scheduling; and the relation between SAT and BDDs for equivalence checking. No subject index. Annotation copyrighted by Book News Inc, Portland, OR.

Proceedings, 2004. **Integrated Inductors and Transformers**. Egidio Ragonese, Angelo Scuderi, Tonio Biondi, Giuseppe Palmisano, 2010-11-15. With the ability to improve performance, reduce fabrication costs, and increase integration levels of both RX and TX sections of the RF/mm-wave front end, passive inductive components have experienced extraordinary growth in ICs. Therefore, a fundamental understanding of monolithic inductors and transformers has become essential for all process engineers.

ICMTS 2002, 2002. **ARFTG Conference Digest**, 2002. **The British National Bibliography**. Arthur James Wells, 2004. **IBM Journal of Research and Development**, 2003. *International Aerospace Abstracts*, 1999. **IEEE International Conference on Electronics, Circuits and Systems**, 2002. **The GEC Journal of Technology**, 1997. **Electrical Performance of Electronic Packaging**, 2004.

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