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Bernhard Steffen (Eds.)

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Recent Insights and Advances



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Wenbin Ji

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Formal Methods and Models for System Design Rajesh Gupta, Paul Le Guernic, Sandeep Kumar Shukla, Jean-Pierre Talpin, 2004-10-01 Perhaps nothing characterizes the inherent heterogeneity in embedded systems than the ability to choose between hardware and software implementations of a given system function. Indeed, most embedded systems at their core represent a careful division and design of hardware and software parts of the system. To do this task effectively, models and methods are necessary: functionality to capture application behavior, needs and system implementation constraints. Formal modeling can be valuable in addressing these tasks. As with most engineering domains, co-design practice defines the state of the art; it seeks to add new capabilities in system conceptualization, model transformation, optimization and implementation. These advances, particularly those related to synthesis and verification tasks, directly depend upon formal understanding of system behavior and performance measures. Current practice in system modeling relies upon exploiting high-level programming frameworks such as SystemC, Esterel, to capture design at increasingly higher levels of abstraction and attempts to reduce the system implementation task. While raising the abstraction levels for design and verification tasks to be really useful, these approaches must also provide for reuse/adaptation of the existing intellectual property (IP) blocks.

Smart Card Research and Advanced Applications Josep Domingo-Ferrer, David Chan, Anthony Watson, 2013-03-14 Smart cards or IC cards offer a huge potential for information processing purposes. The portability and processing power of IC cards allow for highly secure conditional access and reliable distributed information processing. IC cards that can perform highly sophisticated cryptographic computations are already available. Their application in the financial services and telecom industries are well known. But the potential of IC cards goes well beyond that. Their applicability in mainstream Information Technology and the Networked Economy is limited mainly by our imagination; the information processing power that can be gained by using IC cards remains as yet mostly untapped and is not well understood. Here lies a vast uncovered research area which we are only beginning to assess and which will have a great impact on the eventual success of the technology. The research challenges range from electrical engineering on the hardware side to tailor-made cryptographic applications on the software side and their synergies. This volume comprises the proceedings of the Fourth Working Conference on Smart Card Research and Advanced Applications (CARDIS 2000) which was sponsored by the International Federation for Information Processing (IFIP) and held at the Hewlett-Packard Labs in the United Kingdom in September 2000. CARDIS conferences are unique in that they bring together researchers who are active in all aspects of design of IC cards and related devices and environments, thus stimulating synergy between different research communities from both academia and industry. This volume presents the latest advances in smart card research and applications and will be essential reading for smart card developers, smart card application developers and computer science researchers involved in computer architecture, computer security and cryptography.

Scalable Techniques for Formal Verification Sandip Ray, 2010-06-17 This book is about formal veri

cation that is the use of mathematical reasoning to ensure correct execution of computing systems With the increasing use of computing systems in safety critical and security critical applications it is becoming increasingly important for our well being to ensure that those systems execute correctly Over the last decade formal verification has made significant headway in the analysis of industrial systems particularly in the realm of verification of hardware A key advantage of formal verification is that it provides a mathematical guarantee of their correctness up to the accuracy of formal models and correctness of reasoning tools In the process the analysis can expose subtle design errors Formal verification is particularly effective in finding corner case bugs that are difficult to detect through traditional simulation and testing Nevertheless and in spite of its promise the application of formal verification has so far been limited in an industrial design validation tool flow The difficulties in its large scale adoption include the following 1 deductive verification using theorem provers often involves excessive and prohibitive manual effort and 2 automated decision procedures e.g model checking can quickly hit the bounds of available time and memory This book presents recent advances in formal verification techniques and discusses the applicability of the techniques in ensuring the reliability of large scale systems We deal with the verification of a range of computing systems from sequential programs to concurrent protocols and pipelined machines Embedded Systems Handbook Richard Zurawski, 2018-09-03 Considered a standard industry resource the Embedded Systems Handbook provided researchers and technicians with the authoritative information needed to launch a wealth of diverse applications including those in automotive electronics industrial automated systems and building automation and control Now a new resource is required to report on current developments and provide a technical reference for those looking to move the field forward yet again Divided into two volumes to accommodate this growth the Embedded Systems Handbook Second Edition presents a comprehensive view on this area of computer engineering with a currently appropriate emphasis on developments in networking and applications Those experts directly involved in the creation and evolution of the ideas and technologies presented offer tutorials research surveys and technology overviews that explore cutting edge developments and deployments and identify potential trends This first self contained volume of the handbook Embedded Systems Design and Verification is divided into three sections It begins with a brief introduction to embedded systems design and verification It then provides a comprehensive overview of embedded processors and various aspects of system on chip and FPGA as well as solutions to design challenges The final section explores power aware embedded computing design issues specific to secure embedded systems and web services for embedded devices Those interested in taking their work with embedded systems to the network level should complete their study with the second volume Network Embedded Systems **ICTE in Transportation and Logistics 2019** Egils Ginters, Mario Arturo Ruiz Estrada, Miquel Angel Piera Eroles, 2020-01-30 This proceedings volume explores the latest advances in transport and logistics while also discussing the applications of modern information technologies telecommunications electronics and prospective research methods and analyzing their impacts on society and the

environment which in turn determine the future development of these technologies The book is intended for a broad readership including transport and logistics business planners and technical experts leveraging industry knowledge and facilitating technology adoption in promising business regions and transit corridors such as Ukraine Kazakhstan and others The authors who include policy planners and crafters as well as education and training professionals address various types of intermodal transport such as rail road maritime air etc

Privacy and Identity Management. Data for Better Living: AI and Privacy Michael Friedewald, Melek Önen, Eva Lievens, Stephan Krenn, Samuel Fricker, 2020-03-10 This book contains selected papers presented at the 14th IFIP WG 9.2.9.6.11.7.11.6 SIG 9.2.2 International Summer School on Privacy and Identity Management held in Windisch Switzerland in August 2019 The 22 full papers included in this volume were carefully reviewed and selected from 31 submissions Also included are reviewed papers summarizing the results of workshops and tutorials that were held at the Summer School as well as papers contributed by several of the invited speakers The papers combine interdisciplinary approaches to bring together a host of perspectives which are reflected in the topical sections language and privacy law ethics and AI biometrics and privacy tools supporting data protection compliance privacy classification and security assessment privacy enhancing technologies in specific contexts The chapters What Does Your Gaze Reveal About You On the Privacy Implications of Eye Tracking and Privacy Implications of Voice and Speech Analysis Information Disclosure by Inference are open access under a CC BY 4.0 license at link.springer.com

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Ernst-Rüdiger Olderog, Bernhard Steffen, 2003-06-26 Computers are gaining more and more control over systems that we use or rely on in our daily lives privately as well as professionally In safety critical applications as well as in others it is of paramount importance that systems controlled by a computer or computing systems themselves reliably behave in accordance with the specification and requirements in other words here correctness of the system of its software and hardware is crucial In order to cope with this challenge software engineers and computer scientists need to understand the foundations of programming how different formal theories are linked together how compilers correctly translate high level programs into machine code and why transformations performed are justifiable This book presents 17 mutually reviewed invited papers organized in sections on methodology programming automation compilation and application

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organized in sections on methodology programming automation compilation and application Communicating Sequential Processes. The First 25 Years Ali E. Abdallah, Cliff B. Jones, Jeff W. Sanders, 2005-05-04 This volume like the symposium CSP25 which gave rise to it commemorates the semi jubilee of Communicating Sequential Processes 1 Tony Hoare's paper Communicating Sequential Processes is today widely regarded as one of the most influential papers in computer science To commemorate it an event was organized under the auspices of BCS FACS the British Computer Society's Formal Aspects of Computing Science specialist group CSP25 was one of a series of such events organized to highlight the use of formal methods emphasize their relevance to modern computing and promote their wider application BCS FACS is proud that Tony Hoare presented his original ideas on CSP at one of its first meetings in 1978 The two day event 7-8 July 2004 was hosted by London South Bank University's Institute for Computing Research Faculty of Business Computing and Information Management The intention was to celebrate retrospect upon and look beyond the first quarter century of CSP

contribution to computer science The meeting examined the impact of CSP on many areas stretching from semantics mathematical models for understanding concurrency and communications and logic for reasoning about behavior through the design of parallel programming languages i o parallelism synchronization and threads to applications varying from distributed software and parallel computing to information security Web services and concurrent hardware circuits It included a panel discussion with panelists Brookes Hoare de Roever and Roscoe chaired by Jeff Sanders poster presentations by PhD students and others featured a re-alarm requiring evacuation in the rain and concluded with the presentation of a fountain pen to Prof Sir C A R Hoare *Tools and Algorithms for the Construction and Analysis of Systems*, 2003

The Handbook of Computer Networks, Distributed Networks, Network Planning, Control, Management, and New Trends and Applications Hossein Bidgoli, 2008 The Handbook of Computer Networks is the third set of reference books from leading author and Professor of Management Information Systems at California State University Bakersfield Hossein Bidgoli The Handbook of Computer Networks is designed to arm researchers practitioners students and managers with in depth understanding of this important and fast growing field in its broadest scope and in an applied and functional framework Each volume incorporates state of the art core information and networking topics practical applications and coverage of the emerging issues in the computer networking and data communications fields **Trustworthy Translation**

for the Requirements State Machine Language Without Events Michael William Whalen, 2005 ACM SIGPLAN Notices, 2006 Chinese Journal of Electronics, 2007 *American Book Publishing Record* R.R. Bowker Company, 1978

Online 72: Conference Proceedings ..., 1973 **Topics in Expert System Design** Giovanni Guida, Carlo Tasso, 1989 Expert Systems are so far the most promising achievement of artificial intelligence research Decision making planning design control supervision and diagnosis are areas where they are showing great potential However the establishment of expert system technology and its actual industrial impact are still limited by the lack of a sound general and reliable design and

construction methodology *Quarterly Research Review* ,1982 *New Insights for Executive Achievement* ,1968

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