

Copper—Fundamental Mechanisms for Microelectronic Applications



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Copper Fundamental Mechanisms For Microelectronic Applications

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Copper Fundamental Mechanisms For Microelectronic Applications:

Copper -- Fundamental Mechanisms for Microelectronic Applications Shyam P. Murarka, Igor V. Verner, Ronald J. Gutmann, 2000-04-06 A complete guide to the state of the art and future direction of copper interconnect technology. Owing to its performance advantages, copper metallization for IC interconnect is attracting tremendous interest in the semiconductor community worldwide. This timely book provides scientists and engineers with a much needed comprehensive reference on the fundamentals and applications of this emerging technology. The authors draw on more than a decade of intimate involvement with copper interconnect research, integrating the vast amounts of available knowledge and making clear the connection between mechanistic principles and relevant technologies. In depth, cutting edge discussions include: The effects of copper in semiconductor materials, especially silicon. The fundamental chemistry and electro chemistry of copper. The effects of copper on insulating materials such as glass and polymers. Intermetallic and interfacial reactions of copper in layered structures. Current and projected applications of copper in integrated circuits. **Copper Fundamental Mechanisms for Microelectronic Applications** also features extensive references, tables, and over 100 illustrations, including dual Damascene patterning necessary for copper interconnects. It is an excellent resource for anyone seeking to explore the current literature and gain insight into opportunities opening in the field.

Advanced Nanoscale ULSI Interconnects: Fundamentals and Applications Yosi Shacham-Diamand, Tetsuya Osaka, Madhav Datta, Takayuki Ohba, 2009-09-19 In **Advanced ULSI interconnects fundamentals and applications**, we bring a comprehensive description of copper based interconnect technology for ultra large scale integration (ULSI) technology for integrated circuit (IC) application. Integrated circuit technology is the base for all modern electronics systems. You can find electronics systems today everywhere, from toys and home appliances to airplanes and space shuttles. Electronics systems, form the hardware that together with software are the bases of the modern information society. The rapid growth and vast exploitation of modern electronics systems create a strong demand for new and improved electronic circuits, as demonstrated by the amazing progress in the field of ULSI technology. This progress is well described by the famous Moore's law, which states in its most general form that all the metrics that describe integrated circuit performance, e.g., speed, number of devices, chip area, improve exponentially as a function of time. For example, the number of components per chip doubles every 18 months, and the critical dimension on a chip has shrunk by 50% every 2 years, on average, in the last 30 years. This rapid growth in integrated circuit technology results in highly complex integrated circuits with an increasing number of interconnects on chips and between the chip and its package. The complexity of the interconnect network on chips involves an increasing number of metal lines per interconnect level, more interconnect levels, and at the same time, a reduction in the interconnect line critical dimensions.

Metal-Dielectric Interfaces in Gigascale Electronics Ming He, Toh-Ming Lu, 2012-02-02 Metal dielectric interfaces are ubiquitous in modern electronics. As advanced gigascale electronic devices continue to shrink, the stability of these interfaces is becoming an increasingly important issue.

that has a profound impact on the operational reliability of these devices In this book the authors present the basic science underlying the thermal and electrical stability of metal dielectric interfaces and its relationship to the operation of advanced interconnect systems in gigascale electronics Interface phenomena including chemical reactions between metals and dielectrics metallic atom diffusion and ion drift are discussed based on fundamental physical and chemical principles Schematic diagrams are provided throughout the book to illustrate interface phenomena and the principles that govern them Metal Dielectric Interfaces in Gigascale Electronics provides a unifying approach to the diverse and sometimes contradictory test results that are reported in the literature on metal dielectric interfaces The goal is to provide readers with a clear account of the relationship between interface science and its applications in interconnect structures The material presented here will also be of interest to those engaged in field effect transistor and memristor device research as well as university researchers and industrial scientists working in the areas of electronic materials processing semiconductor manufacturing memory chips and IC design

ISTFA 2007 Proceedings of the 33rd International Symposium for Testing and Failure Analysis ASM International,2007-01-01 Printbegr nsninger Der kan printes 10 sider ad gangen og max 40 sider pr session

ULSI Process Integration II Cor L. Claeys,Electrochemical Society. Meeting,2001 **High Purity Silicon VIII** Cor L. Claeys,2004 This Proceedings Volume includes papers that were presented at the Eighth Symposium on High Purity Silicon held in Honolulu Hawaii at the 206th Meeting of the Electrochemical Society October 3 8 2004 Pref **The International Handbook on Innovation** Larisa V Shavinina,2003-10-16 The breadth of this work will allow the reader to acquire a comprehensive and panoramic picture of the nature of innovation within a single handbook *Interlayer Dielectrics for Semiconductor Technologies* Shyam P Muraka,Moshe Eizenberg,Ashok K Sinha,2003-10-13 Semiconductor technologies are moving at such a fast pace that new materials are needed in all types of application Manipulating the materials and their properties at atomic dimensions has become a must This book presents the case of interlayer dielectrics materials whilst considering these challenges Interlayer Dielectrics for Semiconductor Technologies cover the science properties and applications of dielectrics their preparation patterning reliability and characterisation followed by the discussion of different materials including those with high dielectric constants and those useful for waveguide applications in optical communications on the chip and the package Brings together for the FIRST time the science and technology of interlayer dielectrics materials in one volume written by renowned experts in the field Provides an up to date starting point in this young research field

Nanodevices and Nanomaterials for Ecological Security Yuri N. Shunin,Arnold E. Kiv,2012-06-05 This book is devoted to a wide range of problems concerning applications of nanomaterials and nanodevices as effective solutions to modern ecological problems Leading experts in nanoscience and nanotechnology present the key theoretical experimental and implementation issues related to the creation and utilization of novel nanoscale devices to help ensure ecological security The authors discuss appropriate nanotechnologies for minimizing various types of risk to human life technogenic risk or

indeed terrorist threats Particular emphasis is placed on defining and studying the required materials properties and in the field on nanoscale devices for sensors and monitoring *Semiconductor Manufacturing Handbook* Hwaiyu Geng, 2005-05-18

WORLD CLASS SEMICONDUCTOR MANUFACTURING EXPERTISE AT YOUR FINGERTIPS This is a comprehensive reference to the semiconductor manufacturing process and ancillary facilities from raw material preparation to packaging and testing applying basics to emerging technologies Readers charged with optimizing the design and performance of manufacturing processes will find all the information necessary to produce the highest quality chips at the lowest price in the shortest time possible The *Semiconductor Manufacturing Handbook* provides leading edge information on semiconductor wafer processes MEMS nanotechnology and FPD plus the latest manufacturing and automation technologies including Yield Management Automated Material Handling System Fab and Cleanroom Design and Operation Gas Abatement and Waste Treatment Management And much more Written by 60 international experts and peer reviewed by a seasoned advisory board this handbook covers the fundamentals of relevant technology and its real life application and operational considerations for planning implementing and controlling manufacturing processes It includes hundreds of detailed illustrations and a list of relevant books technical papers and websites for further research This inclusive wide ranging coverage makes the *Semiconductor Manufacturing Handbook* the most comprehensive single volume reference ever published in the field

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Materials, Processes, Integration and Reliability in Advanced Interconnects for Micro- and Nanoelectronics: Volume 990 Qinghuang Lin, 2007-09-12 The MRS Symposium Proceeding series is an internationally recognised reference suitable for researchers and practitioners [Surface Engineering 2002 - Synthesis, Characterization and Applications:](#)

Volume 750 A. Kumar,2003-06-02 This book provides a forum for international and interdisciplinary discussion of phenomena where materials performance may be enhanced through engineered surfaces and interfaces The materials science of hard and wear resistant coatings including nanostructured coatings is a scientifically challenging problem with immediate technological applications in machine and machining tools magnetic recording and other manufacturing industries Hard coating systems designed for use in next generation materials applications must achieve a combination of physical properties The performance and reliability of MEMS devices strongly depend on the near surface properties and many materials issues at the micro and nanoscale remain to be resolved This book reviews key aspects discusses ongoing research and speculates on future trends Topics include nano and micrometer scale characterization and properties synthesis and characterization of nanostructured materials deposition characterization and properties of films and coatings industrial applications of surface engineering atomistic and continuum modeling of materials properties surface engineering issues for bio microelectronics applications and surface engineering issues in MEMS structures and devices **Materials, Processes and Reliability for Advanced Interconnects for Micro- and Nanoelectronics - 2009: Volume 1156** Martin Gall,2009-11-18 The MRS Symposium Proceeding series is an internationally recognised reference suitable for researchers and practitioners

Chemical-Mechanical Planarization: Volume 767 Duane S. Boning,2003-08-27 Chemical mechanical planarization CMP has emerged as a critical fabrication technology for advanced integrated circuits Even as the applications of CMP have diversified and we have begun to understand aspects of the physics and chemistry of the process a new generation of CMP innovations is unfolding New slurries and consumables are under development New applications to novel devices continue to appear This book the most recent in a successful series on CMP offers a review of the advances to date and provides a comprehensive discussion of the future challenges that must be overcome Presentations from academia government labs and industry are featured Topics include CMP modeling CMP science CMP slurries and particles for planarization of copper oxide and other materials planarization applications including shallow trench isolation STI copper damascene and novel devices and CMP integration **Japanese Journal of Applied Physics** ,2004 **VLSI Circuits and Systems** ,2003

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