

Custom Memory Management Methodology

Exploration of Memory
Organisation for Embedded
Multimedia System Design

*Fransky Ganthoor, Sven Wuytsack, Eddy DeGroel,
Floren Dalens, Lucie Nachtergaele
and Arnold Vandecastelle*

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Custom Memory Management Methodology Exploration Of Memory Organisation For Embedded Multimedia System Design

Youn-Long Steve Lin



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Custom Memory Management Methodology Francky Catthoor, Sven Wuytack, G. E. de Greef, 2014-01-15 **Custom**

Memory Management Methodology Francky Cattthoor, Sven Wuytack, G.E. de Greef, Florin Banica, Lode Nachtergaele, Arnout Vandecappelle, 1998-09-30 The main intention of this book is to give an impression of the state of the art in system level memory management data transfer and storage related issues for complex data dominated real time signal and data processing applications The material is based on research at IMEC in this area in the period 1989 1997 In order to deal with the stringent timing requirements and the data dominated characteristics of this domain we have adopted a target architecture style and a systematic methodology to make the exploration and optimization of such systems feasible Our approach is also very heavily application driven which is illustrated by several realistic demonstrators partly used as red thread examples in the book Moreover the book addresses only the steps above the traditional high level synthesis scheduling and allocation or compilation traditional or ILP oriented tasks The latter are mainly focussed on scalar or scalar stream operations and data where the internal structure of the complex data types is not exploited in contrast to the approaches discussed here The proposed methodologies are largely independent of the level of programmability in the data path and controller so they are valuable for the realisation of both hardware and software systems Our target domain consists of signal and data processing systems which deal with large amounts of data Systematic Methodology for Real-Time Cost-Effective Mapping of Dynamic Concurrent Task-Based Systems on Heterogenous Platforms Zhe Ma, Pol Marchal, Daniele Paolo Scarpazza, Peng Yang, Chun Wong, José Ignacio Gómez, Stefaan Himpe, Chantal Ykman-Couvreur, Francky Catthoor, 2007-08-26 A genuinely useful text that gives an overview of the state of the art in system level design trade off explorations for concurrent tasks running on embedded heterogeneous multiple processors The targeted application domain covers complex embedded real time multi media and communication applications This material is mainly based on research at IMEC and its international university network partners in this area over the last decade In all the material those in the digital signal processing industry will find here is bang up to date **System-on-Chip** Bashir M. Al-Hashimi, 2006-01-31 This book highlights both the key achievements of electronic systems design targeting SoC implementation style and the future challenges presented by the continuing scaling of CMOS technology **High Performance Computing - HiPC 2000** Mateo Valero, Viktor K. Prasanna, Sriram Vajapeyam, 2003-06-29 This book constitutes the refereed proceedings of the 7th International Conference on High Performance Computing HiPC 2000 held in Bangalore India in December 2000 The 46 revised papers presented together with five invited contributions were carefully reviewed and selected from a total of 127 submissions The papers are organized in topical sections on system software algorithms high performance middleware applications cluster computing architecture applied parallel processing networks

wireless and mobile communication systems and large scale data mining

Essential Issues in SOC Design Youn-Long Steve Lin, 2007-05-31 This book originated from a workshop held at the DATE 2005 conference namely Designing Complex SOC's State of the art in issues related to System on Chip SoC design by leading experts in the fields covers IP development verification integration chip implementation testing and software SOC design is fast becoming the key area of focus that engineers and researchers from the Electronic Design Automation field are focusing on in their quest to further develop Integrated Circuit technology The more systems and even networks that we can integrate on one piece of silicon the faster cheaper more powerful and efficient the technology will become Essential Issues in SOC Design contains valuable academic and industrial examples for those involved with the design of complex SOC's all contributors are selected from a region of the world that is generally known to lead the SOC Revolution namely Asia

The Industrial Information Technology Handbook Richard Zurawski, 2018-10-03 The Industrial Information Technology Handbook focuses on existing and emerging industrial applications of IT and on evolving trends that are driven by the needs of companies and by industry led consortia and organizations Emphasizing fast growing areas that have major impacts on industrial automation and enterprise integration the Handbook covers topics such as industrial communication technology sensors and embedded systems The book is organized into two parts Part 1 presents material covering new and quickly evolving aspects of IT Part 2 introduces cutting edge areas of industrial IT The Handbook presents material in the form of tutorials surveys and technology overviews combining fundamentals and advanced issues with articles grouped into sections for a cohesive and comprehensive presentation The text contains 112 contributed reports by industry experts from government companies at the forefront of development and some of the most renowned academic and research institutions worldwide Several of the reports on recent developments actual deployments and trends cover subject matter presented to the public for the first time

Low-Power Processors and Systems on Chips Christian Piguet, 2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet's recently published Low Power Electronics Design this volume addresses the design of low power microprocessors in deep submicron technologies It provides a focused reference for specialists involved in systems on chips from low power microprocessors to DSP cores reconfigurable processors memories ad hoc networks and embedded software Low Power Processors and Systems on Chips is organized into three broad sections for convenient access The first section examines the design of digital signal processors for embedded applications and techniques for reducing dynamic and static power at the electrical and system levels The second part describes several aspects of low power systems on chips including hardware and embedded software aspects efficient data storage networks on chips and applications such as routing strategies in wireless RF sensing and actuating devices The final section discusses embedded software issues including details on compilers retargetable compilers and coverification tools Providing detailed examinations contributed by leading experts Low Power Processors and Systems

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