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Editors

Current Trends in Hardware Verification and Automated Theorem Proving



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John Robert Harrison



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Current Trends in Hardware Verification and Automated Theorem Proving Graham Birtwistle,P.A.

Subrahmanyam,2012-12-06 This report describes the partially completed correctness proof of the Viper block model Viper 7 8 9 11 23 is a microprocessor designed by W J Cullyer C Pygott and J Kershaw at the Royal Signals and Radar Establishment in Malvern England henceforth RSRE for use in safety critical applications such as civil aviation and nuclear power plant control It is currently finding uses in areas such as the deployment of weapons from tactical aircraft To support safety critical applications Viper has a particularly simple design about which it is relatively easy to reason using current techniques and models The designers who deserve much credit for the promotion of formal methods intended from the start that Viper be formally verified Their idea was to model Viper in a sequence of decreasingly abstract levels each of which concentrated on some aspect of the design such as the flow of control the processing of instructions and so on That is each model would be a specification of the next less abstract model and an implementation of the previous model if any The verification effort would then be simplified by being structured according to the sequence of abstraction levels These models or levels of description were characterized by the design team The first two levels and part of the third were written by them in a logical language amenable to reasoning and proof Higher Order Logic Theorem Proving and Its Applications Jeffrey J. Joyce, Carl-Johan H.

Seger,1994-04-28 This volume constitutes the refereed proceedings of the 1993 Higher Order Logic Users Group Workshop held at the University of British Columbia in August 1993 The workshop was sponsored by the Centre for Integrated Computer System Research It was the sixth in the series of annual international workshops dedicated to the topic of Higher Order Logic theorem proving its usage in the HOL system and its applications The volume contains 40 papers including an invited paper by David Parnas McMaster University Canada entitled Some theorems we should prove **Automated**

Deduction - A Basis for Applications Volume I Foundations - Calculi and Methods Volume II Systems and Implementation Techniques Volume III Applications Wolfgang Bibel,P.H. Schmitt,2013-03-09 We are invited to deal

with mathematical activity in a systematic way one does expect and look for pleasant surprises in this requirement of a novel combination of psychology logic mathematics and technology Hao Wang 1970 quoted from Wang 1970 The field of mathematics has been a key application area for automated theorem proving from the start in fact the very first automatically found theorem was that the sum of two even numbers is even Davis 1983 The field of automated deduction has witnessed considerable progress and in the last decade automated deduction methods have made their way into many areas of research and product development in computer science For instance deduction systems are increasingly used in software and hardware verification to ensure the correctness of computer hardware and computer programs with respect to a given specification Logic programming while still falling somewhat short of its expectations is now widely used deductive databases are well developed and logic based description and analysis of hardware and software is commonplace today **2nd**

Workshop on Libraries, Component Modeling and Quality Assurance Eugenio Villar Bonet,1997 Compendio de los trabajos presentados en Toledo durante el 2nd Workshop on Libraries component modeling and quality assurance

Designing Correct Circuits Geraint Jones,Mary Sheeran,2013-12-14 These proceedings contain the papers presented at a workshop on Designing Correct Circuits jointly organised by the Universities of Oxford and Glasgow and held in Oxford on 26 28 September 1990 There is a growing interest in the application to hardware design of the techniques of software engineering As the complexity of hardware systems grows and as the cost both in money and time of making design errors becomes more apparent so there is an eagerness to build on the success of mathematical techniques in program development The harsher constraints on hardware designers mean both that there is a greater need for good abstractions and rigorous assurances of the trustworthiness of designs and also that there is greater reason to expect that these benefits can be realised The papers presented at this workshop consider the application of mathematics to hardware design at several different levels of abstraction At the lowest level of this spectrum Zhou and Hoare show how to describe and reason about synchronous switching circuits using UNiLY a formalism that was developed for reasoning about parallel programs Aagaard and Leaser use standard mathematical techniques to prove correct their implementation of an algorithm for Boolean simplification The circuits generated by their formal synthesis system are thus correct by construction Thuau and Pilaud show how the declarative language LUSTRE which was designed for programming real time systems can be used to specify synchronous circuits

Formal Methods and Software Engineering Shengchao Qin,Zongyan Qiu,2011-10-12 This book constitutes the refereed proceedings of the 13th International Conference on Formal Engineering Methods ICFEM 2011 held in Durham UK October 2011 The 40 revised full papers together with 3 invited talks presented were carefully reviewed and selected from 103 submissions The papers address all current issues in formal methods and their applications in software engineering They are organized in topical sections on formal models model checking and probability specification and development security formal verification cyber physical systems event B verification analysis and testing refinement as well as theorem proving and rewriting

Computational Logic Dov M. Gabbay,Jörg H. Siekmann,John Woods,2014-12-09 Handbook of the History of Logic brings to the development of logic the best in modern techniques of historical and interpretative scholarship Computational logic was born in the twentieth century and evolved in close symbiosis with the advent of the first electronic computers and the growing importance of computer science informatics and artificial intelligence With more than ten thousand people working in research and development of logic and logic related methods with several dozen international conferences and several times as many workshops addressing the growing richness and diversity of the field and with the foundational role and importance these methods now assume in mathematics computer science artificial intelligence cognitive science linguistics law and many engineering fields where logic related techniques are used inter alia to state and settle correctness issues the field has diversified in ways that even the pure logicians working in

the early decades of the twentieth century could have hardly anticipated Logical calculi which capture an important aspect of human thought are now amenable to investigation with mathematical rigour and computational support and fertilized the early dreams of mechanised reasoning *Calculus* The Dartmouth Conference in 1956 generally considered as the birthplace of artificial intelligence raised explicitly the hopes for the new possibilities that the advent of electronic computing machinery offered logical statements could now be executed on a machine with all the far reaching consequences that ultimately led to logic programming deduction systems for mathematics and engineering logical design and verification of computer software and hardware deductive databases and software synthesis as well as logical techniques for analysis in the field of mechanical engineering This volume covers some of the main subareas of computational logic and its applications Chapters by leading authorities in the field Provides a forum where philosophers and scientists interact Comprehensive reference source on the history of logic

Reconfigurable Embedded Control Systems: Applications for Flexibility and Agility Khalgui, Mohamed, Hanisch, Hans-Michael, 2010-11-30 This book addresses the development of reconfigurable embedded control systems and describes various problems in this important research area which include static and dynamic manual or automatic reconfigurations multi agent architectures modeling and verification component based approaches architecture description languages distributed reconfigurable architectures real time and low power scheduling execution models and the implementation of such systems [Proceedings of the 1991 International Workshop on the HOL Theorem Proving System and Its Applications, August 28-30, 1991, Davis, California](#) Myla Archer, 1992 *Higher Order Logic Theorem Proving and Its Applications*, 1995 **Applied Formal Methods--FM-trends ...**, 1998 *Computer-aided Verification*, 1994

Towards Composition of Verified Hardware Devices E. Thomas Schubert, 1991 *A Methodology for the Formal Verification of Composed Hardware Systems* Edward Thomas Schubert, 1992 *Correct Hardware Design and Verification Methods*, 1995

Theorem Proving with the Real Numbers John Robert Harrison, 1996 Abstract This thesis discusses the use of the real numbers in theorem proving Typically theorem provers only support a few discrete datatypes such as the natural numbers However the availability of the real numbers opens up many interesting and important application areas such as the verification of floating point hardware and hybrid systems It also allows the formalization of many more branches of classical mathematics which is particularly relevant for attempts to inject more rigour into computer algebra systems Our work is conducted in a version of the HOL theorem prover We describe the rigorous definitional construction of the real numbers using a new version of Cantor's method and the formalization of a significant portion of real analysis We also describe an advanced derived decision procedure for the Tarski subset of real algebra as well as some more modest but practically useful tools for automating explicit calculations and routine linear arithmetic reasoning Finally we consider in more detail two interesting application areas We discuss the desirability of combining the rigour of theorem provers with the power and convenience of computer algebra systems and explain a method we have used in practice to achieve this We then

move on to the verification of floating point hardware After a careful discussion of possible correctness specifications we report on two case studies one involving a transcendental function We aim to show that a theory of real numbers is useful in practice and interesting in theory and that the LCF style of theorem proving is well suited to the kind of work we describe We hope also to convince the reader that the kind of mathematics needed for applications is well within the abilities of current theorem proving technology **ACM Transactions on Computer Systems** ,1993 *A HOL Semantics for a Subset of ELLA* Richard Boulton,1992 The proof has been performed in an extension of the HOL system Some proof tools written to support the verification are also described *Research Topics in Functional Programming* D. A. Turner,1990 *VLSI design '91* ,1991

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