Data Flow Architecture for Machine Control (Industrial control, computers & communications series)

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Concurrency Akinori Yonezawa,Takayasu Itō,1991 This volume is a collection of papers on topics focused around concurrency based on research work presented at the UK Japan Workshop held at Wadham College Oxford September 25 27 1989 The volume is organized into four parts Papers on theoretical aspects of concurrency which reflect strong research activities in the UK including theories on CCS and temporal logic RDL Papers on object orientation and concurrent languages which reflect major research activities on concurrency in Japan The languages presented include extensions of C Prolog and Lisp as well as object based concurrent languages Papers on parallel architectures and VLSI logic including a rewrite rule machine a graph rewriting machine and a dataflow architecture An overview of the workshop including the abstracts of the talks and the list of participants The appendix gives a brief report of the first UK Japan Workshop in Computer Science held at Sendai Japan July 6 9 1987 PUBLISHER S WEBSITE

Resources in Parallel and Concurrent Systems ,1991 Computer Systems Organization Parallel architecture

Proceedings of the ... American Control Conference ,1982 **Implementation of Functional Multiprocessors** Olivier Boudillet,Jai Prakash Gupta,Stephen Winter,1991 Describes one example of implementing parallel processing computer technology using multi CPU systems based on commercially available devices Explains the methodology of implementing a distributed operating system based on the lambda calculus details the CTDOS system to handle the reduction mechanism and demonstrates an implementation on a network of transputers For graduate students and researchers in computer science Annotation copyrighted by Book News Inc Portland OR **Parallel Processing** Michel Cosnard,Michael H. Barton,Marco Vanneschi,1988 The emphasis of this book is on architectures with parallelism local memory and message passing communication mechanisms New algorithms for various applications are described such as matrix computations combinatorics image processing dynamic programming and symbolic computations New parallel architectures including systolic arrays for dedicated applications are presented and models of parallel computations Petri Nets statistical modeling and distributed algorithms are addressed Information Processing International Federation for Information Processing,1989 **Frontiers'96, the Sixth Symposium on the Frontiers of Massively Parallel Computation : October 27-31, 1996, Annapolis, Maryland : Proceedings** IEEE Computer Society,1996 Papers from the October 1996 symposium combine perspectives on architecture applications and systems with special focus on future systems concepts especially petaflops computing Includes sections on scheduling and routing applications and algorithms petaflops computing and point design studies SIMD I O techniques memory management synchronization networks and performance analysis Specific subjects include a quasi barrier technique to improve performance of an irregular application hardware controlled prefeching in directory based cache coherent systems and point designs for 100 TF computers using PIM technologies No index Annotation copyrighted by Book News Inc Portland OR **Proceedings of the ... Annual Parallel Processing Symposium** ,1989 The Cumulative Book Index ,1990 A world list of books in the English language *Bibliographic*

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