

# Lecture Notes in Computer Science

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Wolfgang Polak

Compiler Specification  
and Verification



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# Compiler Specification And Verification

**Kenneth J. Turner**



## **Compiler Specification And Verification:**

*Compiler Specification and Verification* Wolfgang Polak, 1981-10      Compiler Specification and Verification W. Polak, 2014-01-15      *Compiler Construction* Uwe Kastens, Peter Pfahler, 1992-09-23 The International Workshop on Compiler Construction provides a forum for the presentation and discussion of recent developments in the area of compiler construction. Its scope ranges from compilation methods and tools to implementation techniques for specific requirements of languages and target architectures. This volume contains the papers selected for presentation at the 4th International Workshop on Compiler Construction CC 92 held in Paderborn, Germany, October 5-7, 1992. The papers present recent developments on such topics as structural and semantic analysis, code generation and optimization and compilation for parallel architectures and for functional, logical and application languages.

Modular Specification and Verification of Object-Oriented Programs Peter Müller, 2002-01-23 Software systems play an increasingly important role in modern societies. Smart cards for personal identification, e-banking, software-controlled medical tools, airbags in cars and autopilots for aircraft control are only some examples that illustrate how everyday life depends on the good behavior of software. Consequently, techniques and methods for the development of high-quality dependable software systems are a central research topic in computer science. A fundamental approach to this area is to use formal specification and verification. Specification languages allow one to describe the crucial properties of software systems in an abstract, mathematically precise and implementation-independent way. By formal verification one can then prove that an implementation really has the desired specified properties. Although this formal methods approach has been a research topic for more than 30 years, its practical success is still restricted to domains in which development costs are of minor importance. Two aspects are crucial to widen the application area of formal methods. Formal specification techniques have to be smoothly integrated into the software and program development process. The techniques have to be applicable to reusable software components. This way the quality gain can be exploited for more than one system, thereby justifying the higher development costs. Starting from these considerations, Peter Müller has developed new techniques for the formal specification and verification of object-oriented software. The specification techniques are declarative and implementation-independent. They can be used for object-oriented design and programming.

**Collaboration between Human and Artificial Societies** Julian A. Padget, 2006-12-31 The full title of the HCM network project behind this volume is VIM: A virtual multicomputer for symbolic applications. The three strands which bound the network together were parallel systems: advanced compilation techniques and artificial intelligence with a common substrate in the programming language Lisp. The initial aim of the project was to demonstrate how the combination of these three technologies could be used to build a virtual multicomputer, an ephemeral/persistent machine of available heterogeneous computing resources for large-scale symbolic applications. The system would support a virtual processor abstraction to distribute data and tasks across the multicomputer, the actual physical composition of which may change dynamically. Our

practical objective was to assist in the prototyping of dynamic distributed symbolic applications in artificial intelligence using whatever resources are available probably networked workstations so that the developed program could also be run on more exotic hardware without reprogramming What we had not foreseen at the outset of the project was how agents would unify the strands at the application level as distinct from the system level outlined above It was as a result of the agent in unience that we held two workshops in May and December 1997 with the title Collaboration between human and artificial societies The papers collected in this volume are a selection from presentations made at those two workshops In each case the format consisted of a number of invited speakers plus presentations from the network partners

**Of what Use is a Verified Compiler Specification?** Paul Curzon,1992      Compiler Construction ,1992      **Piton** J Strother Moore,2007-08-28

Mountaineers use pitons to protect themselves from falls The lead climber wears a harness to which a rope is tied As the climber ascends the rope is paid out by a partner on the ground As described thus far the climber receives no protection from the rope or the partner However the climber generally carries several spike like pitons and stops when possible to drive one into a small crack or crevice in the rock face After climbing just above the piton the climber clips the rope to the piton using slings and carabiners A subsequent fall would result in the climber hanging from the piton if the piton stays in the rock the slings and carabiners do not fail the rope does not break the partner is holding the rope taut and secure and the climber had not climbed too high above the piton before falling The climber's safety clearly depends on all of the components of the system But the piton is distinguished because it connects the natural to the artificial In 1987 I designed an assembly level language for Warren Hunt's FM8501 verified microprocessor I wanted the language to be conveniently used as the object code produced by verified compilers Thus I envisioned the language as the first software link in a trusted chain from verified hardware to verified applications programs Thinking of the hardware as the rock I named the language Piton

**Foundations of Software Technology and Theoretical Computer Science** ,2001      GWAI ... ,1981      *Design and Implementation of Symbolic Computation Systems* ,1993      FME '97 Industrial Applications and Strengthened Foundations of Formal Methods John Fitzgerald,Cliff B. Jones,Peter Lucas,1997-09-03 This book constitutes the refereed proceedings of FME 97 the 4th International Symposium of Formal Methods Europe devoted to Industrial Applications and Strengthened Foundations of Formal Methods held in Graz Austria in September 1997 The 35 revised full papers presented in the volume were selected from a total of 94 submissions Formal methods and mathematically based techniques are increasingly recognized as a viable technology for the development and engineering of computing systems The majority of the papers in this volume describe industrial applications extensions to existing techniques or case studies papers on theoretical aspects show clear potential applicability

**Higher Order Logic Theorem Proving and Its Applications** Jeffrey J. Joyce,Carl-Johan H. Seger,1994-04-28 This volume constitutes the refereed proceedings of the 1993 Higher Order Logic User's Group Workshop held at the University of British Columbia in August 1993 The workshop was sponsored by the Centre for

Integrated Computer System Research It was the sixth in the series of annual international workshops dedicated to the topic of Higher Order Logic theorem proving its usage in the HOL system and its applications The volume contains 40 papers including an invited paper by David Parnas McMaster University Canada entitled Some theorems we should prove A Verified Compiler for a Verified Microprocessor Jeffrey J. Joyce,1989 *Dissertation Abstracts International* ,1993 **A Verified Vista Implementation** Paul Curzon,1993 Abstract We describe the formal verification of a simple compiler using the HOL theorem proving system The language and microprocessor considered are a subset of the structured assembly language Vista and the VIPER microprocessor respectively We describe how our work is directly applicable to a family of languages and compilers and discuss how the correctness theorem and verified compiler fit into a wider context of ensuring that object code is correct We first show how the compiler correctness result can be formally combined with a proof system for application programs We then show how our verified compiler despite not being written in a traditional programming language can be used to produce compiled code We also discuss how a dependable implementation might be obtained **Computers, Control & Information Theory** ,1981 **Abstract State Machines** ,2000 *Proceedings* ,1987 *Formal Description Techniques* Kenneth J. Turner,1989 Software tools papers Software tools demonstrations Object oriented design with FDTs Language issues arising practical use Theoretical aspects of analysis Experience in writing formal descriptions Practical aspects of analysis

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