Compact Transistor Modelling for Circuit Design (Computational Microelectronics)

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De Graaff, H. C.

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Compact Transistor Modelling For Circuit Design Computational Microelectronics

E. Jan W. ter Maten, Hans-Georg Brachtendorf, Roland Pulch, Wim Schoenmaker, Herbert De Gersem

Compact Transistor Modelling For Circuit Design Computational Microelectronics:

Compact Transistor Modelling for Circuit Design Henk C. de Graaff, François M. Klaassen, 2012-12-06 During the first decade following the invention of the transistor progress in semiconductor device technology advanced rapidly due to an effective synergy of technological discoveries and physical understanding Through physical reasoning a feeling for the right assumption and the correct interpretation of experimental findings a small group of pioneers conceived the major analytic design equations which are currently to be found in numerous textbooks Naturally with the growth of specific applications the description of some characteristic properties became more complicated For instance in integrated circuits this was due in part to the use of a wider bias range the addition of inherent parasitic elements and the occurrence of multi dimensional effects in smaller devices Since powerful computing aids became available at the same time complicated situations in complex configurations could be analyzed by useful numerical techniques Despite the resulting progress in device optimization the above approach fails to provide a required compact set of device design and process control rules and a compact circuit model for the analysis of large scale electronic designs This book therefore takes up the original thread to some extent Taking into account new physical effects and introducing useful but correct simplifying assumptions the previous concepts of analytic device models have been extended to describe the characteristics of modern integrated circuit devices This has been made possible by making extensive use of exact numerical results to gain insight into complicated situations of transistor operation The Monte Carlo Method for Semiconductor Device Simulation Carlo Jacoboni, Paolo Lugli, 1989-10-30 This volume presents the application of the Monte Carlo method to the simulation of semiconductor devices reviewing the physics of transport in semiconductors followed by an introduction to the physics of semiconductor devices

Operation and Modeling of the MOS Transistor Yannis Tsividis, Colin McAndrew, 2011 The MOS Metal Oxide
Semiconductor transistor is the most important building block of modern silicon integrated circuits This book fills an important gap in the literature by presenting a unified treatment of the operation and modeling of the MOS transistor that is complemented with extensive intuitive discussions The MOS transistor is the dominant VLSI Very Large Scale Integration device and understanding of this device is mandatory for those people planning a career in device physics and modeling as well as in circuit design Especially important for university courses there is a logical systematic and progressive description that starts with semiconductor fundamentals and builds up to a comprehensive understanding of the basics of MOS transistors For practicing professionals there are details of nuances observed in MOS transistor behavior and various approaches to modeling these are presented Detailed derivations are given for modeling dc currents charges for large signal operation small signal operation at low frequencies and high frequencies and noise Compact Models for Integrated Circuit Design Conventional Transistors and Beyond provides a modern treatise on compact models for circuit computer aided design CAD Written by an author with

more than 25 years of industry experience in semiconductor processes devices and circuit CAD and more than 10 years of academic experience in teaching compact modeling courses this first of its kind book on compact SPICE models for very large scale integrated VLSI chip design offers a balanced presentation of compact modeling crucial for addressing current modeling challenges and understanding new models for emerging devices Starting from basic semiconductor physics and covering state of the art device regimes from conventional micron to nanometer this text Presents industry standard models for bipolar junction transistors BJTs metal oxide semiconductor MOS field effect transistors FETs FinFETs and tunnel field effect transistors TFETs along with statistical MOS models Discusses the major issue of process variability which severely impacts device and circuit performance in advanced technologies and requires statistical compact models Promotes further research of the evolution and development of compact models for VLSI circuit design and analysis Supplies fundamental and practical knowledge necessary for efficient integrated circuit IC design using nanoscale devices Includes exercise problems at the end of each chapter and extensive references at the end of the book Compact Models for Integrated Circuit Design Conventional Transistors and Beyond is intended for senior undergraduate and graduate courses in electrical and electronics engineering as well as for researchers and practitioners working in the area of electron devices However even those unfamiliar with semiconductor physics gain a solid grasp of compact modeling concepts from this book Coupled Problems Solutions E. Jan W. ter Maten, Hans-Georg Brachtendorf, Roland Pulch, Wim Schoenmaker, Herbert De Gersem, 2019-11-06 Designs in nanoelectronics often lead to challenging simulation problems and include strong feedback couplings Industry demands provisions for variability in order to guarantee quality and yield It also requires the incorporation of higher abstraction levels to allow for system simulation in order to shorten the design cycles while at the same time preserving accuracy The methods developed here promote a methodology for circuit and system level modelling and simulation based on best practice rules which are used to deal with coupled electromagnetic field circuit heat problems as well as coupled electro thermal stress problems that emerge in nanoelectronic designs This book covers 1 advanced monolithic multirate co simulation techniques which are combined with envelope wavelet approaches to create efficient and robust simulation techniques for strongly coupled systems that exploit the different dynamics of sub systems within multiphysics problems and which allow designers to predict reliability and ageing 2 new generalized techniques in Uncertainty Quantification UQ for coupled problems to include a variability capability such that robust design and optimization worst case analysis and yield estimation with tiny failure probabilities are possible including large deviations like 6 sigma 3 enhanced sparse parametric Model Order Reduction techniques with a posteriori error estimation for coupled problems and for UQ to reduce the complexity of the sub systems while ensuring that the operational and coupling parameters can still be varied and that the reduced models offer higher abstraction levels that can be efficiently simulated All the new algorithms produced were implemented transferred and tested by the EDA vendor MAGWEL Validation was

conducted on industrial designs provided by end users from the semiconductor industry who shared their feedback contributed to the measurements and supplied both material data and process data In closing a thorough comparison to measurements on real devices was made in order to demonstrate the algorithms industrial applicability **16th Nordic Semiconductor Meeting** Hafliði Gíslason, Vidar Gudmundsson, 1994 **Springer Handbook of** Semiconductor Devices Massimo Rudan, Rossella Brunetti, Susanna Reggiani, 2022-11-10 This Springer Handbook comprehensively covers the topic of semiconductor devices embracing all aspects from theoretical background to fabrication modeling and applications Nearly 100 leading scientists from industry and academia were selected to write the handbook s chapters which were conceived for professionals and practitioners material scientists physicists and electrical engineers working at universities industrial R D and manufacturers Starting from the description of the relevant technological aspects and fabrication steps the handbook proceeds with a section fully devoted to the main conventional semiconductor devices like e g bipolar transistors and MOS capacitors and transistors used in the production of the standard integrated circuits and the corresponding physical models In the subsequent chapters the scaling issues of the semiconductor device technology are addressed followed by the description of novel concept based semiconductor devices The last section illustrates the numerical simulation methods ranging from the fabrication processes to the device performances Each chapter is self contained and refers to related topics treated in other chapters when necessary so that the reader interested in a specific subject can easily identify a personal reading path through the vast contents of the handbook Introduction to **Electronic Devices** Michael Shur,1996 This book gives readers enough knowledge of the required principles so they can adapt the knowledge to new materials and types of devices As device dimensions shrink and more exotic compound semiconductor materials are used in electronic circuits the physics involved in understanding the device behavior becomes more complicated and more fascinating American Book Publishing Record ,1991 CMOS/BiCMOS ULSI Kiat Seng Yeo, Samir S. Rofail, Wang-Ling Goh, 2002 For upper level and graduate level Electrical and Computer Engineering courses in Integrated Circuit Design as well as professional circuit designers engineers and researchers working in portable wireless communications hardware This book presents the fundamentals of Complementary Metal Oxide Semiconductor CMOS and Bipolar compatible Complementary Metal Oxide Semiconductor BiCMOS technology as well as the latest technological advances in the field It discusses the concepts and techniques of new integrated circuit design for building high performance and low power circuits and systems for current and future very large scale integration VLSI and giga scale integration GSI applications CMOS BiCMOS ULSI Low Voltage Low Power is an essential resource for every professional moving toward lower voltage lower power and higher performance VLSI circuits and subsystems design **Proceedings Bibliographic Guide to Technology** New York Public Library. Research Libraries, 1978 ,1995 **Sustainable Energy** and Fuels Piush Verma, Ahmed Boubakeur, Leila Mokhnache, Balwinder Raj, 2024-10-30 Sustainability refers to the concept

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Describes the basics of nanoelectronic technology and the structure of popular electronic systems Reveals the techniques required for design excellence and manufacturability FinFET Modeling for IC Simulation and Design Yogesh Singh Chauhan, Darsen Lu, Sriramkumar Vanugopalan, Sourabh Khandelwal, Juan Pablo Duarte, Navid Payvadosi, Ali Niknejad, Chenming Hu, 2015-03-17 This book is the first to explain FinFET modeling for IC simulation and the industry standard BSIM CMG describing the rush in demand for advancing the technology from planar to 3D architecture as now enabled by the approved industry standard The book gives a strong foundation on the physics and operation of FinFET details aspects of the BSIM CMG model such as surface potential charge and current calculations and includes a dedicated chapter on parameter extraction procedures providing a step by step approach for the efficient extraction of model parameters With this book you will learn Why you should use FinFET The physics and operation of FinFET Details of the FinFET standard model BSIM CMG Parameter extraction in BSIM CMG FinFET circuit design and simulation Authored by the lead inventor and developer of FinFET and developers of the BSIM CM standard model providing an experts insight into the specifications of the standard The first book on the industry standard FinFET model BSIM CMG Oesterreichische Bibliographie ,1990 Electrical & Electronics Abstracts ,1997 **Compact Modeling** Gennady Gildenblat, 2010-06-22 Most of the recent texts on compact modeling are limited to a particular class of semiconductor devices and do not provide comprehensive coverage of the field Having a single comprehensive reference for the compact models of most commonly used semiconductor devices both active and passive represents a significant advantage for the reader Indeed several kinds of semiconductor devices are routinely encountered in a single IC design or in a single modeling support group Compact Modeling includes mostly the material that after several years of IC design applications has been found both theoretically sound and practically significant Assigning the individual chapters to the groups responsible for the definitive work on the subject assures the highest possible degree of expertise on each of the covered models Science Abstracts .1995

2001 International Conference on Modeling and Simulation of Microsystems Matthew Laudon, Bartlomiej F.
Romanowicz, 2001 The worlds most comprehensive and up to date collection of Multidisciplinary Micro and Nano technical papers Technical Proceedings of the 2001 International Conference on Modeling and Simulation of Microsystems Micro and Nano Fluidic Systems MEMS System Optimization MEMS Applications and Characterization Advanced Numerics Process Modeling Quantum Effects Quantum Devices Spintronics Atomistic of Silicon Processing Advanced Semiconductors Circuit Modeling Compact Modeling Papers taken from the 2001 MSM Hilton Head Island USA March 2001

Unveiling the Magic of Words: A Review of "Compact Transistor Modelling For Circuit Design Computational Microelectronics"

In a world defined by information and interconnectivity, the enchanting power of words has acquired unparalleled significance. Their ability to kindle emotions, provoke contemplation, and ignite transformative change is actually awe-inspiring. Enter the realm of "Compact Transistor Modelling For Circuit Design Computational Microelectronics," a mesmerizing literary masterpiece penned with a distinguished author, guiding readers on a profound journey to unravel the secrets and potential hidden within every word. In this critique, we shall delve into the book is central themes, examine its distinctive writing style, and assess its profound impact on the souls of its readers.

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