

World Scientific Series in Computer Science — Vol. 6

DC FLUX PARAMETRON

A New Approach to Josephson Junction Logic

E Goto & K F Loe

Dc Flux Parametron

A Loxley



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DC Flux Parametron Kia Fock Loe, Eiichi Goto, 1986 The direct current flux parametron using Josephson junction and based on the principle of parametron seems to be the answer for the search of new technology for the future needs of computer power The combination of its very high switching speed and highly integrable Josephson junction with the established parametron computer technology accounts for its high performance This book presents the new type of Josephson computer technology which is fundamentally different from the conventional approach In this approach a logic state is represented by a flux polarity whereas the conventional approach uses a voltage state to represent a logic state

Quantum Flux Parametron Eiichi Goto, Willy Hioe, 1991-03-30 This book concerns a Josephson device for supercomputers which has extremely low heat dissipation about 106 times less than semiconductor devices and 103 times less than voltage based Josephson devices In the previous book on Quantum Flux Parametrons QFPs DC Flux Parametron the basic device operation are described This book deals in much greater depth on the problems which are faced by the QFP The device characteristics are worked out in detail showing clearly the analysis methods used A new logic gate using the QFP is described with respect to its basic scheme operation and ways for forming logic circuits The problems faced by the basic QFP are much reduced in the new logic gate As the QFP operates near the Heisenberg and Boltzmann limits for computing devices we also show the relationship between speed and stability The book contains the latest analytical results on QFPs The material presented in the book can be understood with very little mathematical training or knowledge about superconducting physics It is also self contained and does not require reading of other material Most of the device characteristics can be reproduced from the equations given using simple programs A circuit simulator is not needed except for high speeds when transient behavior becomes important *Issues In Josephson Supercomputer Design - Proceedings Of The 6th And 7th Riken Symposia On Josephson Electronics* Eiichi Goto, Kia Fock Loe, 1991-03-15 Quantum Flux Parametron QFP is a Josephson junction device which uses the polarities of a unit of quantum flux to represent a bit of binary information The low power consumption and the inductive nature of signal transfer of QFP promise the realizability of a highly integrated Josephson Supercomputer The miniaturization and the inductively connected circuits pose new challenges and problems to the designers of such a computer system This volume contains five papers which provide detailed illustrations of the essential problems in the design of Josephson Supercomputers The problems include i the three dimensional packaging problems in the highly integrated system ii the detection of trapped magnetic quanta iii the computation of three dimensional inductance and iv the elimination or alleviation of various internal and external operational problems of QFP arising from the limitation of the current fabrication technology as well as the inherent problems of direct coupling of QFP *Official Gazette of the United States Patent and Trademark Office*, 1989 Fluxoid Josephson Computer Technology Rikagaku Kenkyūjo (Japan), 1988 Scientific Papers Tokyo. Institute of Physical and Chemical Research, 1986 *Journal of Electronic*

Engineering ,1989 **High Temperature Superconductivity in Japan** ,1990 **IEDM Technical Digest** ,1987 **A**
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 Rikagaku Kenkyūjo (Japan),1986 Beginning with v 12 its Abstracts v 1 16 from its Bulletin v 7 22 were issued with the
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International Conference on Neural Networks, Sheraton Harbor Island East, San Diego, California, June 21-24,
1987 Maureen Caudill,1987 *Advances in Superconductivity X* Kōzō Osamura,Izumi Hirabayashi,1998 **Japanese**
Journal of Applied Physics ,1991 *Neural Information Processing and VLSI* Bing J. Sheu,Joongho Choi,1995-02-28
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